

Mandatory Disclosure

Format for Mandatory Disclosure	
AICTE File No.	F.No. Eastern/1-9317933873/2021/EOA
Date & Period of last approval	Date: 25-Jun-2021

Name of the Institution	CALCUTTA INSTITUTE OF TECHNOLOGY
Address of the Institution	BANITABLA, ULUBERIA, HOWRAH
City & Pin Code	HOWRAH-711316
State / UT	WEST BENGAL
Phone number with STD code	033-26610736
FAX number with STD code	033-26613733
Email	principal.cit@urschard.in
Website	www.urschard.in

Name of the organization running the Institution	ULUBERARURALSOCIETYFORCAREOFHEALTH& RESEARCHDEVELOPMENT
Address of the organization	ANJALIAPARTMENTCENEMATALA,BAZARPARA,ULUBERIA, HOWRAH-711316
Contact No.	033 2661-0736
E-Mail	info@urschard.in
Website of the organization	www.urschard.in

Name of the affiliating University	MaulanaAbulKalamazad University of Technology formerly WBUT
Address	NH-12, HARINGHATA, P.O. SIMHAT, PIN: 741249
Website	https://makautwb.ac.in/

Name of Principal / Director	Prof. PriyabrataSarkar
Exact Designation	Principal –In- Charge
Address	BANITABLA, ULUBERIA, HOWRAH-711316
Phone number with STD code	033-2661-0736
FAX number with STD code	033-26613733
Email	principal.cit@urschard.in

Governance Officer of the Institute	1. Prof. (Dr) Priyabrata Sarkar (Principal-In-Charge)
-------------------------------------	---

Governing Board Members	http://cit.urschard.in/governing_body
Frequency of meetings & date of last meeting	02 Nos. & 17 th Dec,2021

Academic Council Members Detail	i) Mr. RahulDebnath,Secretary ii) Prof. PriyabrataSarkar, PrincipalInCharge iii) Dr. Monal Dutta,TIC,CHE iv) Mr. Krishna Kumar Jha,TIC,CSE v) Dr. Tuli Baukshi, TIC, Gen.Sc&Hu vi) Dr. Himadri Mandal,TIC,ECE vii) Mr. Partha Sarathi Ghosh,TIC,ME viii) Mr. Ganesh Tiwary, TIC,EE ix) Mr. ,Bikram Paul, HOD,CE
---------------------------------	---

Board of Studies	Prof. Subhasis Maity, Chairman, Maulana Abul Kalam Azad University Of Technology, W.B.
------------------	---

Members of Academic Advisory Body	<ol style="list-style-type: none"> 1. Mr. Rahul Debnath, Chairman 2. Prof. Priyabrata Sarkar, Convener 3. Dr. Monal Dutta, Member 4. Mr. Krishna Kumar Jha, Member 5. Dr. Tuli Baukshi, Member 6. Dr. Himadri Mandal, Member 7. Mr. Partha Sarathi Ghosh, Member 8. Mr. Ganesh Tiwary, Member 9. Mr. Bikram Paul, Member
Frequency of meetings & date of last meeting	Half yearly

Organisational Chart	<p style="text-align: center;"><u>ORGANISATIONAL STRUCTURE</u></p> <pre> graph TD Society[Society] --> GB[Governing Body] GB --> Principal[Principal] Principal --> HODS[HOD'S] Principal --> AO[Administrative Officer] HODS --> Acad[Professor Associate. Professor Asst. Professor Lab. Asstt. Tech. Asst.] AO --> Admin[Admin Staffs] </pre>
----------------------	--

Nature and Extent of Involvement of Faculty and Students in Academic Affairs / Improvements	<p>Faculty: a. Teaching b. Research c. Mentoring d. Plant Visit e. Project guidance f. Tutorial g. Grooming h. Conference & Seminars</p> <p>Students: Participations in a. Plant Visit b. Industrial Training c. Campus Interview d. Model competition</p>
---	--

Mechanism / Norms & Procedures for Democratic/ Good Governance	<p>The Institute ensures that the rules are followed by students as well as Teachers. The governance is given to all teaching faculty and students by 1. Principal 2. HODs/TICs 3. Mentors 4. Class Representatives</p> <p>For prevention of ragging there is active anti-ragging cell, for prevention of harassment of women employees, active CC works. The grievances are addressed through Grievance Redressal Cell. There are prominent postings of Names of all members of the cells with contact numbers for reporting of any incident.</p>
--	--

Student Feedback on Institutional Governance/ Faculty performance	Students give their feedbacks in the website, through mails to faculty members. The feedback is good so far. The faculty performance is also very good with respect to number of teachers joining PhD programs and their involvement in research, conferences, seminars and teachers' training programs (AICTE) and awards of PhDs and sponsored projects.
Grievance redressal mechanism for faculty, staff and students	The complainant (student, staff member, faculty member) may submit the complaint/grievance either by hard copy or mail to the Chairperson/Convener of the GRC. The convener gives notice for discussion on the grievances and if required, calls the complainant to listen to him/her and ask to produce evidences if any. Based on the discussion, documents, the resolution is drawn and informed to the complainant and also to the Management for quick redressal. If the grievances are found out of scope of the GRC, it is forwarded to the Management indicating the same and also the complainant is informed of the same.

Establishment of Anti-Ragging Committee	Yes, the details are also given in the website. Regular meetings are held by the Anti-ragging squad and minutes sent to the Directorate of Technical Education, West Bengal. https://cit.urschard.in/uploads/notice/achivement_1617605910.pdf
Establishment of Online Grievance Redressal Mechanism	Yes, the details are also given in the website. Regular meetings are held by the Anti-ragging squad and minutes sent to the Directorate of Technical Education, West Bengal.
Establishment of Grievance Redressal Committee in the Institution and Appointment of OMBUDSMAN by the University	The GRC has been formed and display of the details of its members is made in the Institute as well Website of the Institute. To facilitate redressal of students' grievances, a complaint box for students has also been kept outside the door of the Chairperson's office. OMBUDSMAN can only be appointed by the University and we have sent request to MAKAUT to form the Registry in our Campus. However, there has been no response from MAKAUT. http://cit.urschard.in/uploads/notice/notice_1548419412.pdf
Establishment of Internal Complaint Committee (ICC)	Yes. The ICC has been formed strictly as per AICTE rules and displayed with details of all members in the Institute premises as well as website. The chairperson of the committee convenes meetings upon receipt of complaints. So far three cases have been registered and resolved. http://cit.urschard.in/uploads/notice/notice_1548419505.pdf
Establishment of Committee for SC/ST	Yes, the SC/ST committee has been formed and the details are given in the website. The posting of these committee members with details will be done in the Institute very soon. http://cit.urschard.in/uploads/notice/notice_1561025662.pdf
Internal Quality Assurance Cell	Yes, IQAC has been formed and notice has been served to all concerned to convene meeting on its future proceedings.

Details of Each programme			
(a) UG Programme (B Tech)	Department	Intake	Duration
	ECE	30	04 YRS
	CSE	60	04 YRS
	CHE	60	04 YRS
	CE	60	04 YRS
	ME	30	04 YRS
	EE	30	04 YRS
Eligibility & Admission Procedure in B.Tech:	H.S. (10+2) examination of the Council of Higher Secondary Education, West Bengal or equivalent (PCM Min. 45%) Qualified Joint Entrance Examination conducted by West Bengal Joint Entrance Examinations Board / JEE (Main) conducted by Central Board of Secondary Education and recommended by the WB JEE Board.		

(b) PG Programme(M.Tech.)			Department		Intake	Duration
Eligibility & Admission Procedure in M.Tech:			CHE		18	2YRS.
			AICTErecognizedBachelor'sDegreeinengineering/Technologyor Equivalent Admissions are made from candidates qualified in PGET (Post Graduate Entrance Examination) conducted by the affiliating University (MAKAUT,WB) followed by centralized counseling process of MAKAUT, WB			
(c) PG Programme (MCA)			Department		Intake	Duration
			MCA		60	2YRS
			Bachelor'sDegreeinengineering/TechnologyorEquivalentwith10+2 Math Qualified(JECA)conductedbyWestBengalJointEntranceExaminations Board			
(d) Diploma Programme(2 nd shift)			Department		Intake	Duration
			DETCE		60	3yrs.
			DEE		60	3yrs.
			DME		60	3yrs.
			DCE		60	3yrs.
Eligibility & Admission Procedure in Diploma:			Passed in 10 th Stadard with at least 35% marks in aggregate (except additionalsubject),inthequalifyexaminationMath,Physicalsc.&English areMandatory			
			Qualified JEXPO Exam conducted by WBSCTE&VE&SD			
Cut-off mark / Ranking for Admission in B.Tech last three years (WBJEE & JEE MAINS)						
Stream	2021-22		2020-21		2019-20	
	First Rank	Last Rank	First Rank	Last Rank	First Rank	Last Rank
ECE			31899	67821	41357	77283
CSE	5708	62013	13860	72000	11113	78823
CHE	12797	60260	24067	64877	37125	76112
CE			48961	48961	35096	71078
ME	57615	-	39900	95456	47283	61896
EE	9184	59246	-	-	31696	72945
Cut-off mark / Ranking for Admission in PG last three years						
Stream	2021-22		2020-21		2019-20	
	First Rank	Last Rank	First Rank	Last Rank	First Rank	Last Rank
CHE	-	-	-	-	-	-
MCA	584	814	49	1553	253	1684
Cut-off mark/RankingforAdmissioninDiplomaEngg.lastthree years						
Stream	2021-22		2020-21		2019-20	
	First Rank	Last Rank	First Rank	Last Rank	First Rank	Last Rank
DETCE	NA	NA	16799	80248	24437	69984
DEE	12810	24964	14270	78938	2464	73464
DME	25723	27260	9734	77203	12374	73092
DCE	NA	NA	19264	79300	9390	64953

Placement facilities	The integral component of the institute is Training & Placement Cell which actively works throughout the year for grooming the final year students to equip them facing personal interview (PI), group discussion (GD), technical interview, HR round etc. The cell organizes industrial training programs for pre-final
----------------------	--

	students in various industries and training organizations. The cell functions independently in consultation with every respective departmental committee. The cell provides immense opportunity to every individual candidate to appear for campus recruitment drives. http://cit.urschard.in/placements		
Faculty			
Branch wise list Faculty members	Sheet Attached		
Faculty : Student ratio	UG - 1:20		
	PG - 1:10		
	Diploma - 1:25		
No. of faculty employed and left during last three years	(a) Left-00 (b) Employed-05		
FeesStructureofB.Tech/M.Tech/Diploma/MCA	http://cit.urschard.in/fees_structure		
Estimated cost of boarding and lodging (Per Month approx)	4500/-		
Last three years admission status in under all categories (UG)			
CSE			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	42+30	14+ 14 (LE)	18 + 03 (LE)
ECE			
Year wise Sanctioned Intake	30 2021-22	30 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	0+15	03+ 24 (LE)	03+ 14 (LE)
CHE			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	18+63	03+ 30 (LE)	06+ 26 (LE)
EE			
Year wise Sanctioned Intake	30 2021-22	30 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	02+33	00+ 53 (LE)	08+ 18 (LE)
ME			
Year wise Sanctioned Intake	30 2021-22	30 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	03+32	01+ 47 (LE)	03+ 16 (LE)
CE			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	0+ 65	01+ 13 (LE)	04+ 21 (LE)

Last three years admission status under all categories (PG)			
CHE			
Year wise Sanctioned Intake	09 2021-22	09 2020-21	18 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	03	00	-
MCA			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	60	56	27+ 04 (LE)
Diploma Engg. ETCE			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	09+ 02	11	7
DEE			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	20 + 27	25	39
DCE			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	06 + 04	5	16
DME			
Year wise Sanctioned Intake	60 2021-22	60 2020-21	60 2019-20
Year wise Actual Admissions(including TFW and Lateral Entry schemes)	20 + 18	22	26
Admission Procedure			
Mention the admission test being followed, name and address of the Test Agency and its URL and itsURL	For UG Qualified Joint Entrance Examination conducted by West Bengal Joint Entrance Examination Board (www.wbjeeb.nic.in)/ JEE(Main) conducted by Central Board of Secondary Education (www.cbse.nic.in) and finally through central online counseling by WBJEEBoard. For Diploma Qualified JEXPO Exam conducted by WBSCTE&VE&SD(www.wbscte.in) For MCA Qualified (JECA) conducted by West Bengal Joint Entrance Examinations Board (www.cbse.nic.in) For M.Tech qualified in PGET (Post Graduate Entrance Examination) conducted by the affiliating University (MAKAUT,WB) followed by centralized counseling process of MAKAUT, WB (www.wbut.ac.in)		
Number of seats allotted to different Test Qualified candidate separately	For UG 80% seats to be filled up by JEE candidates, 10% by JEE(Main) candidates and 10% under Management Quota for candidates qualified through WBJEE / JEE (Main) Lateral Entry of Diploma Holder and B.Sc. degree holder through JELET conducted by WBJEEB, in 2 nd year with an additional intake 20% of the approve intake.		
Calendar for admission against Management / Vacant seats			
Last date of request for applications	AspernormslaiddownbyDirectorofTechnicalEducationGovt.ofWestBengal,WestBeng alJointEntranceExaminationBoardandaffiliatingUniversity(MAKAUT,WB)		
Last date of submission of applications			

Dates for announcing final results	
Release of admission list	
Date for acceptance by the candidate	
Last date for closing of admission	
The waiting list shall be activated only on the expiry of date of main list	
Starting of the Academic session	
Starting of the Academic session	As per MAKAUT, WB Academic Calender
Refund Policy	Caution deposit will be refunded as per norms
Describe each criterion with its respective weightages i.e. Admission Test, marks in qualifying examination etc.	As per norms laid down by Director of Technical Education, Govt. of West Bengal, West Bengal Joint Entrance Examination Board and affiliating University (MAKAUT, WB)
Mention the minimum level of acceptance, if any	
Mention the cut-off levelsof percentage and percentile score of the candidates in the admission test for the last three years	
Display marks scored in Test etc. and in aggregate for all candidates who were admitted	
Information of Infrastructure and Other Resources Available	
Number of Class Rooms and size of each	22 and 69.66 Sq.M
Number of Tutorial rooms and size of each	12 and 38.75 Sq.M
Number of Laboratories and size of each	39 and 68.67 Sq.M
Number of Drawing Halls with capacity of each	1 and 70 capacity
Number of Computer Centres with capacity of each	1 and 70 capacity
Central Examination Facility, Number of rooms and capacity of each	
Barrier Free Built Environment for disabled and elderly persons	YES
Fire and Safety Certificate	YES
Hostel Facilities	YES

Library		
NumberofLibraryBooks/Titles / Journals available	TITLE-6200 VOLUME-52000	
List of online National/ International Journals subscribed	N.J- 20 I.J-07	
E- Library facilities	STM ONLINE JOURNAL	
	BOOK BANK FACILITY AVAILABLE	
Laboratory and Workshop		
Name Of The Laboratory	Major Equipments	
Applied Mechanics Lab	Universal testing machine Impact tesler (izod testy charpy)machine Tensile tester machine Hardness tester machine Tartion tester machine Cam & follower Governors	
Workshop	Patterns Moulding tools Surface roughness tester Lathe machine, milling machine, grindness Drilling machine SMAW and gas welding equipment(machine) Forging setup Power hacksaw	
Thermal Lab	Heat exchanger Shell & tube apparatus Emissivity measurement Stefan boltzman apparatus Metalred/insulating power calculating of thermal conductivity apparatus	
Metrology lab	Vernier height gauge Filler ,slip,dialgauge Sinebar Strain gauge Depthmicrometer	
Fluid lab	VenturimeterOrifice neterReynod's apparatus Pitot tube Piton &francis turbine Centrifugal & jet pump Pipe fraction (laminan/turbulrnt) apparatus	
Highway lab	Ductility test apparatus Ws-angles abbnesion test Bitumensofteningpointtest(ringbulltest) Marsnw stabilitytest Impact testapparatus	
Survay Lab	Leveling instrument Plune table Thcodolite Chuin survey instrument	
Soil Lab	Triuxiul test apparatus Uncondined comp test	

	C.B.R test Direct shear test Consolidation test
PROJECT LAB	XILINX ISE8.1I, JAVA, SE
PROGRAMMING PRACTISE LAB	OPEN JAVA, DOSBOX
OS LAB	FEDORA CORE LINUX VERSION 24
DBMS LAB	ORACLE LOG, MICROSOFT VB
Software: Matlab & PSpice	<ol style="list-style-type: none"> 1. Characteristics of Series & Parallel Resonant circuits 2. Verification of Network Theorems 3. Transient Response in R-L & R-C Networks; simulation/hardware 4. Transient Response in RLC Series & Parallel Circuits & Networks; simulation/hardware 5. Determination of Impedance (Z), and Admittance (Y) parameters of Two-port networks 6. Generation of periodic, exponential, sinusoidal, damped sinusoidal, step, impulse, and ramp signals using MATLAB 7. Representation of Poles and Zeros in s-plane, determination of partial fraction expansion in s-domain and cascade connection of second-order systems using MATLAB 8. Determination of Laplace Transform, different time domain functions, and Inverse Laplace Transformation using MATLAB
Solid State Device Lab	<ol style="list-style-type: none"> 1. Study input characteristics of BJT in common-emitter configuration. 2. Study output characteristics of BJT in common-emitter configuration for different base currents and hence determine hybrid parameters. 3. Study output characteristics of BJT in common-emitter configuration and find performance parameters (Voltage Gain, Current Gain, Input Impedance, Output Impedance). 4. Study the variation of small-signal voltage gain with frequency of a common-emitter RC coupled amplifier. 5. Study of drain characteristics and transfer characteristics of a JFET and hence determine the FET parameters (drain resistance, transconductance & amplification factor). 6. Study the variation of small-signal voltage gain with frequency of a JFET.
Signals & Systems Lab	<ol style="list-style-type: none"> 1. To study Z-transform of: a) Sinusoidal signals b) Step functions. 2. To compare Fourier and Laplace transformations of a signal. 3. To study convolution theorem in time and frequency domain. 4. To Study Signal Synthesis via sum of harmonics. 5. To study LPF & HPF, band pass and reject filters using RC circuits. 6. To demonstrate how analog signals are sampled and how different sampling rates affect the outputs. 7. To study sampling theorem for low pass signals and band pass signals. 8. To determine the components of: a) Square wave b) Clipped sine wave.
Analog Electronic Circuits Lab (Room – 210)	<ol style="list-style-type: none"> 1. Study of Diode as clipper & clamper 2. Study of Zener diode as a voltage regulator 3. Study of ripple and regulation characteristics of full wave rectifier without and with capacitor filter 4. Study of characteristics curves of BJT & FET. 5. Design a two-stage RC coupled amplifier & study of its gain & Bandwidth. 6. Study of class A & class B power amplifiers. 7. Study of class C & Push-Pull amplifiers. 8. Realization of current mirror & level shifter circuit using Operational Amplifiers. 9. Study of timer circuit using NE555 & configuration for monostable & astable multivibrator. 10. Design a Bistable multivibrator using NE555. 11. Study of Switched Mode Power Supply & construction of a linear voltage regulator using IC chip. 12. Design a simple function generator using IC. 13. Realization of a V-to-I & I-to-V converter using Op-Amps. 14. Realization of a Phase Locked Loop using Voltage Controlled Oscillator (VCO). 15. Study of D.A.C & A.D.C.


Analog Communication Lab	<ol style="list-style-type: none"> 1. Measurement of modulation index of an AM signal. 2. measurement of output power with varying modulation index an AM signal (for both DSB-&SSB). 3. Measurement of distortion of the demodulated output with varying modulation index of an AM signal (for both DSB-SC&SSB). 4. Measurement of power of different frequency components of a frequency modulated signal & the measurement of the bandwidth. 5. Design a PLL using VCO & to measure the lock frequency. 6. Design a FM demodulator using PLL. 7. Measurement of SNR of a RF amplifier. 8. Measurement of selectivity, sensitivity, fidelity of a superhetrodyne receiver. 9. Study of waveforms of various functional points (output of RF, IF & video) of a B/W TV receiver. 10. Study of the vertical & horizontal sweep of the time base unit of a B/W TV. 11. One innovative experiment.
Microprocessors & Microcontrollers Lab	<ol style="list-style-type: none"> 1. Study of prewritten programs on trainer kit using the basic instruction set (data transfer, Load/Store, Arithmetic, Logical) Assignments based on above. 2. Familiarization with 8085 & 8051 simulator on PC. Study of prewritten programs using basic instruction set (data transfer, Load/Store, Arithmetic, Logical) on the simulator. Assignments based on above. 3. Programming using kit and simulator for: <ol style="list-style-type: none"> i) Table lookup ii) Copying a block of memory iii) Shifting a block of memory iv) Packing and unpacking of BCD numbers v) Addition of BCD numbers vi) Binary to ASCII conversion vii) String Matching, Multiplication using shift and add method and Booth's Algorithm 4. Program using subroutine calls and IN/OUT instructions using 8255 PPI on the trainer kit e.g. subroutine for delay, reading switch state and glowing LEDs accordingly. 5. Study of timing diagram of an instruction on oscilloscope. 6. Interfacing of 8255: Keyboard and Multi-digit Display with multiplexing using 8255. 7. Study of 8051 Microcontroller kit and writing programs as mentioned in S/L3. Write programs to interface of Keyboard, DAC and ADC using the kit. 8. Serial communication between two trainer kits.
Control System Lab	<ol style="list-style-type: none"> 1. Familiarization with MATLAB Control System toolbox, MATLAB-SIMULINK tool box & pSPICE. 2. Determination of step response for 1st order & 2nd order system with a unity feedback on CRO & calculation of control system specifications for variations of system design. 3. Simulation of step response & impulse response for Type-I & Type-II system with unity feedback using MATLAB & pSPICE. 4. Determination of root locus, Bode-plot, Nyquist Plot, using MATLAB control system toolbox for a given 2nd order transfer function & determination of different control system specifications. 5. Determination of PI, PD, and PID controller action on 1st order simulated process. 6. Determination of approximate transfer function experimentally using Bode Plot. 7. Evaluation of steady-state error, setting time, percentage peak overshoots, gain margin, phase margin with addition of lead compensator in forward path transfer functions using MATLAB & pSPICE. 8. Study of position control system using servomotor. 9. Design and hardware implementation of a temperature controller using microprocessor/microcontroller.
Microelectronics & VLSI Designs Lab	<ol style="list-style-type: none"> 1. Familiarity with Spice simulation tool. 2. Spice Simulation of Inverter, NAND, NOR Gates. 3. Familiarity with EDA tools for VLSI design / FPGA based system design 4. Layouts, Transistors and tools. 5. Standard cell Design 6. Design of CMOS XOR/XNOR Gates. 7. Design of CMOS Fulladder 8. Design of CMOS Flip flops (R-S, D, J-K)

	10. Design of 8 bit synchronous Counter. 11. Design of 8 bit bi-directional register with tri-stated input/output bus. 12. Design of a 12 bit CPU with few instructions and implementation and validation on FPGA
RF & Microwave Engg. Lab	1. Determination of phase and group velocities in a waveguide carrying TE ₁₀ Wave from Dispersion diagram [ω - β Plot]. 2. Measurement of unknown impedance using shift in minima technique using a waveguide test bench/ Measurement of the susceptance of an inductive and/or a capacitive window using shift in minima technique using a waveguide test bench 3. Study of the characteristics of a Reflex Klystron oscillator 4. Study of Gunn oscillator Characteristics using X-band waveguide test bench. 5. Measurement of coupling factor, Directivity, Insertion loss and Isolation of a Directional coupler using X-band waveguide test bench setup. 6. Scattering matrix of a magic tee/E-plane tee/H-plane tee using waveguide test bench at X-band. 7. Experimental/Simulation Study of filter (LPF, HPF, BPF) response. 8. Measuring of dielectric constant of a material using waveguide test bench at X-band.
EM Theory & Transmission Lines Lab	1. Plotting of Standing Wave Pattern along a transmission line when the line is open-circuited, short-circuited and terminated by a resistive load at the load end. 2. Input Impedance of a terminated coaxial line using shift in minima technique. 3. Study of Smith chart on Matlab platform. 4. Simulation study of Smith chart - Single and double stub matching. 5. Radiation Pattern of dipole antenna. 6. Radiation Pattern of a folded-dipole antenna. 7. Radiation pattern of a 3-element Yagi-Uda Antenna. 8. Beamwidth, gain and radiation pattern of a 3-element, 5-element and 7-element Yagi-Uda antenna - Comparative study. 9. Radiation pattern, Gain, Directivity of a Pyramidal Horn Antenna. 10. Study of Spectrum Analyzer.
Digital Electronic & Integrated Circuits Lab	1. Realization of basic gates using Universal logic gates. 2. Code conversion circuits-BCD to Excess-3 and vice-versa. 3. Four-bit parity generator and comparator circuits. 4. Construction of simple Decoder and Multiplexer circuits using logic gates. 5. Design of combinational circuit for BCD to decimal conversion to drive 7-segment display using multiplexer. 6. Construction of simple arithmetic circuits-Adder, Subtractor. 7. Realization of RS-JK and D flip-flops using Universal logic gates. 8. Realization of Universal Register using JK flip-flops and logic gates. 9. Realization of Universal Register using multiplexer and flip-flops. 10. Construction of Adder circuit using Shift Register and full Adder. 11. Realization of Asynchronous Up/Down counter. 12. Realization of Synchronous Up/Down counter. 13. Design of Sequential Counter with irregular sequences. 14. Realization of Ring counter and Johnson's counter. 15. Construction of adder circuit using Shift Register and full Adder.
Digital Communication Lab	1) Design, implementation and study of all the properties of 7-length and 15-length pn sequences using shift register. 2) Study of PAM and demodulation. 3) Study of PCM and demodulation. 4) Study of line coders: polar/unipolar/bipolar NRZ, RZ and Manchester. 5) Study of delta modulator and demodulator. 6) Study of adaptive delta modulator and demodulator. 7) Study of BPSK modulator and demodulator. 8) Study of BFSK modulator and demodulator. 9) Study of ASK modulator and demodulator. 10) Study of QPSK modulator and demodulator. 11) Simulation study of probability of symbol error for BPSK modulation. 12) Simulation study of probability of symbol error for BFSK modulation.
Digital Signal Processing Lab	Simulation Laboratory using standard Simulator: 1. Sampled sinusoidal signal, various sequences and different arithmetic operations. 2. Convolution of two sequences using graphical methods and using


	<p>commands- verification of the properties of convolution.</p> <p>3. Z-transform of various sequences – verification of the properties of Z-transform.</p> <p>4. Twiddle factors – verification of the properties.</p> <p>5. DFTs/IDFTs using matrix multiplication and also using commands.</p> <p>6. Circular convolution of two sequences using graphical methods and using commands, differentiation between linear and circular convolutions.</p> <p>7. Verification of the differential algorithms associated with filtering of long data sequences and Overlap – add and Overlap – save methods.</p> <p>8. Butterworth filter design with different set of parameters.</p> <p>9. FIR filter design using rectangular, Hamming and Blackman windows.</p> <p>Hardware Laboratory using either 5416 or 6713 Processor and Xilinx FPGA:</p> <p>1. Writing & execution of small programs related to arithmetic operations and convolution using Assembly Language of TMS320C 5416/6713 Processor, study of MAC instruction.</p> <p>2. Writing of small programs in VHDL and downloading onto Xilinx FPGA.</p> <p>3. Mapping of some DSP algorithms onto FPGA.</p>
System Design Lab	<p>Solar Oven</p> <p>Solar Cooker</p> <p>Wind Turbine Bed</p> <p>Solar Water Heater</p> <p>Power Generation transmission Model</p>
Power Electronics lab	<p>PLC kit</p> <p>Regenerative, Breaking, Dynamic breaking Kit</p> <p>Phase Sequence</p>
Power System Lab	<p>MI COM relay</p> <p>Oil Cup and Gauge (For Oil test)</p> <p>Active power and reactive power flow kit</p> <p>Active power and reactive power control kit by alternative Kit</p> <p>Voltage Relay, Earth Fault Relay, On time delay relay and off time delay relay kit</p>
Chemistry Lab 1 & 2	<p>HOT air oven</p> <p>Polarimeter</p> <p>Muffle Furnace</p> <p>Colorimeter</p> <p>Spectrophotometer</p> <p>Gas Cylinders</p>
Computing Facilities	
Internet Bandwidth	20 MBPS
Number and configuration of System	300 , CORi3 – 3.0 GHz, RAM 4GB, HD 500 GB, 15.6’’
Total number of system connected by LAN	ALL
Total number of system connected by WAN	01
Major software packages available	YES
Special purpose facilities available	YES
List of facilities available	
Games and Sports Facilities	We have a playground and facilities for games such as Football, Cricket, Badminton, Volleyball.
Extra-Curricular Activities	Plant visit, Blood donation camp, Excursion, Yoga & meditation, Tree plantation
Soft Skill Development Facilities	We have Language laboratory and skilled faculty members for the soft skill development of students.
Teaching Learning Process	
Curricula and syllabus for each of the programmes as approved by the University	http://www.wbut.ac.in/page.php?id=195
Academic Calendar of the University	http://www.wbut.ac.in/page.php?id=229
Academic Time Table with the name of the Faculty members handling the Course	DATA AVAILABLE IN RESPECTIVE DEPARTMENTS
Teaching Load of each Faculty	DATA AVAILABLE IN RESPECTIVE DEPARTMENTS
Internal Continuous Evaluation System and place	As directed by the affiliating University i.e. MAKAUT, presently two slot examinations per Semester are taken to internally assess the students
Student’s assessment of Faculty, System in place	It is being implemented
List of Research Projects / Consultancy Works	

Number of Projects carried out, funding agency, Grant received	ONE (received in April, 2019) Duration till April, 2022 Project Title: Ultra-sensitive simultaneous detection of heavy metals in air borne particulates via electrochemical analysis Funding agency: DST-SERB, GOVT. of INDIA Grant received : Rs52Lacs
Publications (if any) out of research in last three years out of masters/Bachelors projects	Electrochemical Column Cell for Continuous Oxidative Inactivation of Pathogens and Reductive Removal of Toxic Heavy Metals Krishnendu Pramanik*, Pavel Sengupta, Shalini Dasgupta, Pallab Datta and Priyabrata Sarkar* https://doi.org/10.1021/acsami.1c04471
Industry Linkage	15
MoUs with Industries (minimum 3)	i. Infosys Pvt.Limited(running) ii. DTRONICS Technology, 35 Sarat Bose Lane,Kolkata 700081(being negotiated) iii. ADIBHATTACHARYYAFIVEIFAMILYFUND,18Glenwood Drive, Essex Junction, Vermont, 05452, USA (Being negotiated) iv. WIST WATER SOLUTIONS PVT. LTD., Kolkata, India (being negotiated)
LoA and subsequent EoA till the current Academic Year	Yes
Accounted audited statement for the last three years	Yes

PRINCIPAL & FACULTY PROFILES
PRINCIPAL-IN-CHARGE

i)	Name	PRIYABRATA SARKAR	
ii)	Date of Birth	17/03/1955	
iii)	Unique ID		
iv)	Date of joining	01/07/2017	
v)	Education Qualification	B.Ch.E, M.Ch.E, PhD in Chemical Engineering	
vi)	Work Experience		
	<ul style="list-style-type: none">Teaching	35 years in Jadavpur University, IIT-KGP, CU, CIT	
	<ul style="list-style-type: none">Research	32 years	
	<ul style="list-style-type: none">Industry	04 years	
	<ul style="list-style-type: none">Others	AS HOD in CU, Dean in CIT	
vii)	Area of Specialization	Chemical Engineering	
viii)	Courses taught at Diploma/Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	B.Tech.,M.Tech.	
ix)	Research guidance		
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences	National: 10 International: 60 Conferences: >100	
	<ul style="list-style-type: none">Master		
nnn	<ul style="list-style-type: none">Ph.D.	15	
x)	Projects Carried out	26	
xi)	Patents	03 awarded, 10 applied	
xii)	Technology Transfer	01	
xiii)	Research Publications	70	
xiv)	No. of Books published with details	Book Chapters: 02	


Faculty (Dept-CSE)

i)	Name	SULEKHA KUNDU	
ii)	Date of Birth	03/01/1982	
iii)	Unique ID	1-488288591(AICTE)	
iv)	Date of joining	02/07/2010	
v)	Education Qualification	1.M.Tech in Computer Science & Engineering(CSE) , Narula Institute of Technology,WBUT(MAKAUT). 2.B.Tech in Computer Science & Engineering(CSE), College of Engineering & Management Kolaghat, WBUT(MAKAUT).	
vi)	Work Experience		
	<ul style="list-style-type: none">Teaching	Working as Assistant Professor at Calcutta Institute Of Technology since 02/07/2010 to till date.	
	<ul style="list-style-type: none">Research		
	<ul style="list-style-type: none">Industry		
	<ul style="list-style-type: none">Others		
vii)	Area of Specialization	Computer Science & Engineering(CSE)	
viii)	CoursetaughtatDiploma/Post Diploma/UnderGraduate/Post Graduate/PostGraduateDiploma Level	B.Tech of 2 ND Year & 4 TH Year CSE stream.	
ix)	Research guidance		
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences	2 International Journal Papers	
	<ul style="list-style-type: none">Master		
	<ul style="list-style-type: none">Ph.D.		
x)	Projects Carried out	UG Level: 20	
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


Faculty (Dept-CSE)

i)	Name	DIPAYAN KUMAR GHOSH	
ii)	Date of Birth	21/06/1981	
iii)	Unique ID	1-506930793(AICTE)	
iv)	Date of joining	11/02/2009	
v)	Education Qualification	1.M.Tech in Computer Science & Engineering(CSE) , Haldia Institute of Technology(HIT),WBUT(MAKAUT). 2.B.Tech in Computer Science & Engineering(CSE), Institute of Engineering & Management(IEM), WBUT(MAKAUT).	
vi)	Work Experience		
	<ul style="list-style-type: none">Teaching	Working as Assistant Professor at Calcutta Institute Of Technology since 11/02/2009 to till date.	
	<ul style="list-style-type: none">Research		
	<ul style="list-style-type: none">Industry		
	<ul style="list-style-type: none">Others		
vii)	Area of Specialization	Computer Science & Engineering(CSE)	
viii)	CoursetaughtatDiploma/Post Diploma/UnderGraduate/Post Graduate/PostGraduateDiploma Level	B.Tech of CSE stream of all Four(4) Years &B.Tech of ECE , EE streams only 4 TH Year 7 TH Sem Computer Networking subject.	
ix)	Research guidance		
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences	4 International Journal Papers.	
	<ul style="list-style-type: none">Master		
	<ul style="list-style-type: none">Ph.D.		
x)	Projects Carried out	UG Level: 22	
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		

Faculty (Dept-CSE)

i)	Name	MONIKA RANI PATRA DALAI	
ii)	Date of Birth	28/02/1991	
iii)	Unique ID		
iv)	Date of joining	10/08/2017	
v)	Education Qualification	M.Tech CSE UNDER MAKAUT	
vi)	Work Experience	4YRS	
	<ul style="list-style-type: none">Teaching	3YRS	
	<ul style="list-style-type: none">Research	1YRS	
	<ul style="list-style-type: none">Industry	NA	
	<ul style="list-style-type: none">Others	NA	
vii)	Area of Specialization	CSE	
viii)	CoursetaughtatDiploma/Post Diploma/UnderGraduate/Post Graduate/PostGraduateDiploma Level	OPERATION RESEARCH(CS 605A) OBJECT ORIENTED PROGRAMMING(EC605A) OPERATING SYSTEM(CS 603)	
)ix)	Research guidance	2 NOS	
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences	1 INTERNATIONAL JOURNAL	
	<ul style="list-style-type: none">Master		
	<ul style="list-style-type: none">Ph.D.	NA	
x)	Projects Carried out	NA	
xi)	Patents	NA	
xii)	Technology Transfer	NA	
xiii)	Research Publications	NA	
xiv)	No. of Books published with details	NA	


Faculty(Dept-CSE)

i)	Name	POULOMI ROY	
ii)	Date of Birth	31/05/1989	
iii)	Unique ID	1-3614348184 (AICTE)	
iv)	Date of joining	13/02/2018	
v)	Education Qualification	M.TECH (Information Technology) from IEST, SHIBPUR	
vi)	Work Experience	2 Years	
	<ul style="list-style-type: none">Teaching	Working as an Assistant Professor at Calcutta Institute Of Technology from 13-Feb- 2018 to present	
	<ul style="list-style-type: none">Research	NA	
	<ul style="list-style-type: none">Industry	NA	
	<ul style="list-style-type: none">Others	NA	
vii)	Area of Specialization	Database Management System (DBMS), C programming Language, Data Structure, Internet Technology and Web Design.	
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1.Database Management System (B.Tech 3 rd Year) 2.Computer Graphics (B.tech 3 rd Year) 3.Data Structure and Algorithm (B.tech 2 nd Year) 4.Computer Application in Pharmacy (B.Pharm 1 st year)	
ix)	Research guidance	NA	
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences		
	<ul style="list-style-type: none">Master		
	<ul style="list-style-type: none">Ph.D.		
x)	Projects Carried out	Project at UG Level : Online Railway Reservation System Project at PG Level : Meda-Based Biochip Testing	
xi)	Patents	NA	
xii)	Technology Transfer	NA	
xiii)	Research Publications	NA	
xiv)	No. of Books published with details	NA	

Faculty(Dept-CSE)

i)	Name	SOURABH CHANDRA	
ii)	Date of Birth	22/07/1985	
iii)	Unique ID	1-488288579	
iv)	Date of joining	20/04/2010	
v)	Education Qualification	M.TECH (CSE) under MAKAUT	
vi)	Work Experience	10.5 YRS	
	• Teaching	10.5 YRS	
	• Research	NA	
	• Industry	NA	
	• Others	NA	
vii)	Area of Specialization	Computer Science & Engineering	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	<ol style="list-style-type: none"> 1. Data Structure & Algorithm(CSE 2ND YEAR CS 302 ,ECE 3RD YEAR,EE504A) 2. Database Management System(ECE 4TH YEAR EC705C) 3. Software Engineering (CSE 4TH YEAR CS701) 4. Introduction to Computing(CSE,ECE,EE 1ST YEAR CS201) 5. Basic Electronics & Computer Application(PTC203) 	
ix)	Research guidance	UG Level: 20	
	• No. of papers published in National / International Journals/Conferences	<i>International/National Journal Publications: 2</i> <i>National Conference Publication: 1</i> <i>International Conference Publication: 7</i>	
	• Master		
	• Ph.D.	NA	
x)	Projects Carried out		
xi)	Patents	NA	
xii)	Technology Transfer		
xiii)	Research Publications	<i>Total No of Publications: 18</i> <i>International/National Journal Publications: 10 Including 2 no's of SCI Journal Publication (one Final Revision Submitted)</i> <i>international Conference Publication : 8 no's</i>	
xiv)	No. of Books published with details	NA	

Faculty (Dept-CSE)

i)	Name	SUBHENDU SEKHAR PATRA	
ii)	Date of Birth	04/02/1979	
iii)	Unique ID	1-488288559	
iv)	Date of joining	22/07/2008	
v)	Education Qualification	M.E.(Computer Science &Engg.) from W.B.U.T. in house	
vi)	Work Experience	10 year	
	<ul style="list-style-type: none">Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology.	
	<ul style="list-style-type: none">Research		
	<ul style="list-style-type: none">Industry		
	<ul style="list-style-type: none">Others		
vii)	Area of Specialization	Computer Science &Engg.	
viii)	Courses taught at Diploma/Post Diploma/Undergraduate/Post Graduate/Post Graduate Diploma Level	1. Analog & Digital Electronics.(CSE 2 nd year 3 rd sem) 2.Formal Language & Automata Theory.(CSE 2 nd year 4 th sem) 3. Compiler Design.(CSE 4 th year7 th sem) 4. Artificial Intelligence.(CSE 4 th year7 th sem)	
ix)	Research guidance		
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences		
	<ul style="list-style-type: none">Master		
	<ul style="list-style-type: none">Ph.D.		
x)	Projects Carried out	UG-25	
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications	5	
xiv)	No. of Books published with details		

Faculty (Dept-CSE)

Name: DR. SATRUGHNA SINGHA

Date of Birth: 01/01/1976

Mob Number: 9433033706 / 8910902632

Email: satrughna.singha@gmail.com

Unique ID- 1-11044168641

Doctorate Degree: Ph.D. (Thesis Submitted)

Master Degree:M.Tech. (Computer Science and Engineering)

Bachelors Degree:B.Tech. (Computer Science and Engineering)

Other Qualification's: B.Sc.(Hons.) in Physics

Field of Specialization:Reconfigurable Architecture and Computing, Computer Architecture, FPGA Architecture and VLSI Design.

Date of joining the Institute: 23/12/2021

Appointment Type: (REGULAR/ADHOC/CONTRACT): REGULAR


Exact Designation: Assistant Professor

Department: Computer Science and Engineering


Course:B.Tech.

Teaching for UG / PG/ Diploma: UG


Faculty (Dept-CSE)

i)	Name	Mr. KRISHNA KUMAR JHA	
ii)	Date of Birth	03/08/1979	
iii)	Unique ID	1-488252541(AICTE)	
iv)	Date of joining	01/08/2008	
v)	Education Qualification	M.Tech(COMPUTERSCIENCEANDAPPLICATION)fromUNIVERSITYOF CALCUTTA	
vi)	Work Experience		
	<ul style="list-style-type: none">Teaching	11 years	
	<ul style="list-style-type: none">Research	4 years	
	<ul style="list-style-type: none">Industry	2 years in ECPL,kolkata	
	<ul style="list-style-type: none">Others		
vii)	Area of Specialization	Medical image processing, networking.	
viii)	CoursetaughtatDiploma/Post Diploma/UnderGraduate/Post Graduate/PostGraduateDiploma Level	Diploma, B. Tech, MCA	
ix)	Research guidance	2	
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences	5	
	<ul style="list-style-type: none">Master		
	<ul style="list-style-type: none">Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications	4	
xiv)	No. of Books published with details		


Faculty (Dept- ECE)


i)	Name	ANUPAM DEY	
ii)	Date of Birth	28/01/1988	
iii)	Unique ID		
iv)	Date of joining	30/01/2018	
v)	Education Qualification	M.Tech	
vi)	Work Experience	6 years	
	• Teaching	3 years	
	• Research	2 year	
	• Industry	1 year	
	• Others	N/A	
vii)	Area of Specialization	Communication Engineering	
viii)	Courses taught at Diploma/Post Diploma/Undergraduate/Post Graduate/Postgraduate Diploma Level	Analog Communication, Digital Communication, Basic Electronics, Digital Electronics	
ix)	Research guidance	N/A	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	Communication Engineering	
	• Ph.D.	N/A	
x)	Projects Carried out	N/A	
xi)	Patents	N/A	
xii)	Technology Transfer	N/A	
xiii)	Research Publications	N/A	
xiv)	No. of Books published with details	N/A	


Faculty (Dept- ECE)

i)	Name	ANIRUDDHA GHOSH	
ii)	Date of Birth	10/11/1981	
iii)	Unique ID		
iv)	Date of joining	18/01/2011	
v)	Education Qualification	M.E.	
vi)	Work Experience	18 years	
	• Teaching	12 years	
	• Research	6 years	
	• Industry	N/A	
	• Others	N/A	
vii)	Area of Specialization	Embedded System	
viii)	Courses taught at Diploma/Post Diploma/Undergraduate/Post Graduate/Post Graduate Diploma Level	Digital Electronics & Logic Circuits, Analog Communication and Principle of Communication, Microprocessor & Microcontroller, Computer Architecture and Organization, Digital Signal Processor (DSP) and DSP processor Architecture, Signal and System, Control System, Basic Electronics	
ix)	Research guidance	N/A	
	• No. of papers published in National / International Journals/Conferences	Conferences : 2 Journals : 5	
	• Master	Embedded System	
	• Ph.D.	Perusing	
x)	Projects Carried out	UG	
xi)	Patents	N/A	
xii)	Technology Transfer	N/A	
xiii)	Research Publications	<ol style="list-style-type: none"> 1. Aniruddha Ghosh, Satrugna Singha and Amitabha Sinha, "A Novel Architecture of "Mixed Number System MAC Unit" For Digital Signal Processors", International Journal of Computer Science and Communication, Volume-1, Number-2, September 2010, Serials Publications, India. ISSN: 0973-7391 2. Satrugna Singha, Aniruddha Ghosh and Amitabha Sinha, "A New Architecture for FPGA based Implementation of Conversion of Binary to Double Base Number System (DBNS) using Parallel Search Technique", ACM SIGARCH Computer Architecture News, Volume 39, Issue 5, pp. 12-18, December 2011, ACM New York, USA. ISSN: 0163-5964 	

		<ol style="list-style-type: none"> 3. AniruddhaGhosh, SatrughnaSingha and AmitabhaSinha,“ANew ArchitectureforFPGAImplementationofAMACUnitforDigitalSignal Processors using Mixed Number System”, ACM SIGARCH Computer ArchitectureNews,Volume40,Issue2,pp.33-38,May2012,ACMNew York, USA. ISSN:0163-5964 4. AniruddhaGhosh, SatrughnaSingha and AmitabhaSinha, ““Floating PointRNS”-ANewConceptforDesigningtheMACUnitofDigitalSignal Processor”, ACM SIGARCH Computer Architecture News, Volume 40, Issue2,pp.39-43,May2012,ACMNewYork,USA.ISSN:0163-5964 5. AniruddhaGhosh and AmitabhaSinha, "FPGA Implementation of MAC Unit for Double Base Ternary Number System (DBTNS) and its Performance Analysis", International Journal of Computer Applications, Volume 181, Issue 14, pp. 9-22, September 2018, Foundation of ComputerScience(FCS),NY,USA,DOI:10.5120/ijca2018917785. 6. AniruddhaGhosh and AmitabhaSinha, "Performance Analysis of FPGA Based MAC Unit using DBTNS Multiplier & TRNS Adder for Signal ProcessingAlgorithm",AdvancesinImageandVideoProcessing,Volume 6, No. 5, pp. 31-43, October 2018, Society for Science and Education, UnitedKingdom,DOI:http://dx.doi.org/10.14738/aivp.65.5394. 7. AniruddhaGhosh and AmitabhaSinha, "FPGA Implementation of RNS Adder Based MAC Unit in Ternary Value Logic Domain for Signal ProcessingAlgorithmanditsPerformanceAnalysis",EDKCON2018IEEE Electron Device Kolkata Conference, November24-25,2018.
xiv)	No. of Books published with details	N/A


i)	Name	BappaDityaBiswas	
ii)	Date of Birth	05.07.1984	
iii)	Unique ID		
iv)	Date of joining	19/01/2009	
v)	Education Qualification	M.Tech	
vi)	Work Experience	11 years	
	• Teaching	10 years	
	• Research	N/A	
	• Industry	1 year	
	• Others	N/A	
vii)	Area of Specialization	Instrumentation & Control	
viii)	Courses taught at Diploma/Post Diploma/Undergraduate/Post Graduate/Post Graduate Diploma Level	Electronics measurement, Basic Electronics, Computer Networking, Analog Electronics, Digital Electronics, Circuit Theory and Networking, Telecommunication Switching and Network, Data communication and computer Network, Tanner EDA for VLSI.	
ix)	Research guidance	N/A	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	Instrumentation & Control	
	• Ph.D.	N/A	
x)	Projects Carried out	UG	
xi)	Patents	N/A	
xii)	Technology Transfer	N/A	
xiii)	Research Publications	N/A	
xiv)	No. of Books published with details	N/A	

i)	Name	CHIRADEEP GHOSH	
ii)	Date of Birth	18.12.1981	
iii)	Unique ID		
iv)	Date of joining	30/1/2018	
v)	Education Qualification	M.Tech	
vi)	Work Experience	17 years	
	• Teaching	10years	
	• Research	1 year	
	• Industry	4 years	
	• Others	Consultancy : 2 years	
vii)	Area of Specialization	Instrumentation & Control	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	Industrial Instrumentation, Process Control, Instrumentation & Control, Control System, Digital Electronics, Sensors & Transducers, Electronics Measurement, Microprocessor & Microcontroller, Computer Network.	
ix)	Research guidance	N/A	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	Instrumentation & Control	
	• Ph.D.	N/A	
x)	Projects Carried out	UG , PG	
xi)	Patents	N/A	
xii)	Technology Transfer	N/A	
xiii)	Research Publications	N/A	
xiv)	No. of Books published with details	N/A	


i)	Name	Dr. HIMADRI MANDAL	
ii)	Date of Birth	19/02/1982	
iii)	Unique ID		
iv)	Date of joining	19/08/2008	
v)	Education Qualification	PhD	
vi)	Work Experience	17 years	
	• Teaching	11 years	
	• Research	6 years	
	• Industry	N/A	
	• Others	N/A	
vii)	Area of Specialization	VLSI	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	VLSI, Microprocessor, Analog communication, Analog electronics, Basic Electronics.	
ix)	Research guidance	N/A	
	• No. of papers published in National / International Journals/Conferences	Journal : 6 CONFERENCE : 6	
	• Master	VLSI	
	• Ph.D.	Accomplished	
x)	Projects Carried out	UG	
xi)	Patents	N/A	
xii)	Technology Transfer	N/A	
xiii)	Research Publications	<p>JOURNALS:</p> <p>1. Phadikar, A., Mandal, H., and Chiu, T. L., "Parallel hardware implementation of data hiding scheme for quality access control of grayscale image based on FPGA", Multidimensional Systems and Signal Processing, 1-29, 2019, 10.1007/s11045-019-00650-x, IF: 2.088.</p> <p>2. Goutam Kr Maity, Poulami Jana, Himadri Mandal and Tien-Lung Chiu, "Power-aware VLSI design of reversible watermarking for access control", Microsystem Technol. DOI: 10.1007/s00542-019-04342-1, IF: 1.581</p> <p>3. Himadri Mandal, Amit Phadikar, Goutam Kr Maity, and Tien-Lung Chiu, "FPGA based low power hardware for quality access control of compressed grayscale image", Microsystem Technologies, 1-14, DOI: 10.1007/s00542-018-3817-2,</p>	

		<p>IF:1.581</p> <p>4.Amit Phadikar, Goutam Kumar Maity, Tien-Lung Chiu, and HimadriMandal, "FPGAImplementationofLifting-BasedDataHidingScheme forEfficientQuality Access Control of Images", Circuits, Systems, and Signal Processing, 1-27, 2018. DOI: 10.1007/s00034-018-0893-6, IF 1.998.</p> <p>5.HimadriMandal, ShubhenduMaiti, Tien-Lung Chiu, and SankarGangopadhyay. "Mismatch considerations in laser diode to single-mode circular core triangular index fiber excitation via upside down tapered hemispherical microlens on the fiber tip," Optik, 168, 533-540, 2018, IF 1.191.</p> <p>6.Tien-Lung Chiu, HimadriMandal, Mi Zhang, Shun-Po Yang, and Ya-Ting Chuang. "Effects of anodic buffer layer in top-illuminated organic solar cell with silver electrodes." International Journal of Photoenergy, 2013.DOI:10.1155/2013/131984,IF1.547.</p> <p>INTERNATIONAL CONFERENCE:</p> <p>1.Himadri Mandal, Goutam Kr Maity, AmitPhadikar, and Tien-Lung Chiu, "FPGA based low power hardware implementation for quality access control of a compressed grayscale image", International Conference on Computational Intelligence, Communications, and Business Analytics, 416-430, 2017. DOI: 10.1007/978-981-10-6427-2_34</p> <p>2.Himadri Mandal, AmitPhadikar, Goutam Kr Maity, and Tien-Lung Chiu, "FPGA based low power hardware implementation for quality access control of digital image using dither modulation", Devices for Integrated Circuit (DevIC), 642-646, 2017.DOI:10.1109/DEVIC.2017.8074029</p> <p>3.Amit Phadikar, HimadriMandal, Goutam Kr Maity, and Tien-Lung Chiu, "A new modelofQIMdatahidingforqualityaccesscontrolofdigitalimage",International Conference on Soft-Computing and Networks Security (ICSNS), 1-5, 2015. DOI: 10.1109/ICSNS.2015.7292441</p> <p>4.PoulamiJana,AmitPhadikar,GoutamKrMaity,HimadriMandal,andTien-Lung Chiu. "Reversible data hiding for content verification and quality access control of image and its hardware implementation", International Conference on Electrical, Electronics, and Optimization Techniques (ICEEOT), 3324-3329, 2016. DOI: 10.1109/ICEEOT.2016.7755321</p> <p>5.Tien-Lung Chiu, HimadriMandal, Shun-Po Yang, Jiun-Haw Lee, and Niladri Mandal. "Engineering on Ag anode for efficient top-illuminated small molecular photovoltaic", In Control, Instrumentation, Energy and Communication (CIEC), 2014 International Conference on, pp. 111-114. IEEE, 2014. DOI: 10.1109/CIEC.2014.6959060</p> <p>6.Niladri Mandal and HimadriMandal, "Implementation and performance analysis of an efficient FFT pruning algorithm for NC-OFDM based cognitive radio system." International Conference on Communication and Industrial Application (ICCIA), 1-6, 2011.</p>
xiv)	No. of Books published with details	1

Dept-ECE

i)	Name	MAHUYA BASU	
ii)	Date of Birth	02/09/1982	
iii)	Unique ID		
iv)	Date of joining	06/07/2009	
v)	Education Qualification	AMIE	
vi)	Work Experience	13 Years	
	• Teaching	9 years	
	• Research	N/A	
	• Industry	4 Years	
	• Others	N/A	
vii)	Area of Specialization	Digital Electronics, VLSI	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	Digital Electronics, Industrial Electronics, Consumer Electronics.	
ix)	Research guidance	N/A	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	Perusing	
	• Ph.D.	N/A	
x)	Projects Carried out	N/A	
xi)	Patents	N/A	
xii)	Technology Transfer	N/A	
xiii)	Research Publications	N/A	
xiv)	No. of Books published with details	N/A	

Dept-ECE


i)	Name	SuvankarDey	
ii)	Date of Birth	14/04/1992	
iii)	Unique ID		
iv)	Date of joining	13/02/2018	
v)	Education Qualification	B.Tech	
vi)	Work Experience	2 years	
	• Teaching	1 year	
	• Research	N/A	
	• Industry	1 year	
	• Others	N/A	
vii)	Area of Specialization	Communication Engineering	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	Principal of Communication Engineering, Elementary Communication Engineering, Analog Electronics, Industrial Electronics, Electronics Workshop	
ix)	Research guidance	N/A	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	N/A	
	• Ph.D.	N/A	
x)	Projects Carried out	N/A	
xi)	Patents	N/A	
xii)	Technology Transfer	N/A	
xiii)	Research Publications	N/A	
xiv)	No. of Books published with details	N/A	


Dept-EE

i)	Name	Dr. Santosh Kumar Nath		
ii)	Date of Birth	01/07/1945		
iii)	Unique ID	1-1507674309		
iv)	Date of joining	15/12/2012		
v)	Education Qualification	BEE&MEEfromJadovpurUniverty,Ph.D.(Moscow),FIV,FIF,MIAE,MISE,PGDCHE,C. Engg(India)MIInst.W.		
vi)	Work Experience			
	<ul style="list-style-type: none">Teaching	Principal/DutyDirector,DirectorateofIndustrialTraining,Govt.ofWestBengal. Principal, Dream Institute of Technology, Kolkata. Principal, Global Institute of Management & Technology, Krishnagar, Nadia, Professor, CIEM, Tollygunge, Kolkata. Professor, Calcutta Institute of Technology, Uluberia, Howrah.		
	<ul style="list-style-type: none">Research	CircuitdesignofTelevisionandotherCircuit. AtM/sGeorgeElectronics,Kolkata-700034		
	<ul style="list-style-type: none">Industry	Advisor, Ambuja Realty Development Ltd. Kolkata. Duration Two years. Development of Electrical Installation of High rise Building.		
	<ul style="list-style-type: none">Others	Not applicable		
vii)	Area of Specialization	A. SystemdesignintheElectricalMachines,Bio-medicalInstrumentation. B. Teaching andAdministration;and C. Survey andValuationWorks. D. Electrical Lay – out &IlluminationDesign		
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	Machines,ElectricalSystemDesignSystem–IandII,Installation,Testingand Maintenance,		
ix)	Research guidance	Research co- guidance for Ph.D.		
	<ul style="list-style-type: none">No.ofpaperspublishedin National / International Journals/Conferences	5 Nos. Paper published National. 9 Nos. Paper Published International Journals.		
	<ul style="list-style-type: none">Master			
	<ul style="list-style-type: none">Ph.D.			
x)	Projects Carried out	More than 50 Nos. Project carried out including DRDO Projects. Valuation of Two EngineAircraftModelNo.VTCNYofM/SCOallIndia. 33-MVSWindTurbineofM/SPrashantGlobalFinanceLtd.Kolkata. Design of Electrical work of 1750 KVA, 33/11 KV, 1500 KVA, 11/0.433 KV and 350 KVA, 11/0.433 KV Sub-Station at Girola Complex, Dist:-Bastar, M.P. Under Defense Research & Development Organization.		
xi)	Patents	Not applicable		
xii)	Technology Transfer	Not applicable		
xiii)	Research Publications	Development ofEquipmentforIndirectMeasurement and MonitoringofBlood Pressure with Audio Visual Devices under Prof. J. K. Chowdhury and Dr. T. K.Basak,		

		Reader, in the Department of Electrical Engineering, Jadavpur University in 1987.
xiv)	No. of Books published with details	<ul style="list-style-type: none"> • Television Servicing Manual (English); • Solid State Television Servicing Manual (English); • Television Fault Finding (Bengali); • Solid State Television Fault Finding (Bengali); • Colour Television Fault Finding (Bengali); • Colour Television(ET&T Circuit) Fault Finding (Bengali); • VCR, VCP FaultFinding(Bengali); • BasicElectronics,TransistorandPowerElectronics(Bengali);and • Basic Tape–Recorder.

Dept-EE


i)	Name	PRITOM ADHIKARY	
ii)	Date of Birth	26/12/1983	
iii)	Unique ID	1-507010191 (AICTE)	
iv)	Date of joining	01/08/2006	
v)	Education Qualification	M.Tech(Instrumentation & Control) from Calcutta University	
vi)	Work Experience	13 yrs(approx.)	
	• Teaching	<input type="checkbox"/> Works as a Assistant Professor at Calcutta Institute Of Technology (01/07/2011 to continue) <input type="checkbox"/> Work as a Pro-Term Lecturer (01/09/2010 to 30/06/2011) at Calcutta Institute Of Technology. <input type="checkbox"/> Work as a Term Lecturer (01/01/2008 to 31/08/2010) at Calcutta Institute Of Technology. <input type="checkbox"/> Work as a Technical Assistant (01/08/2006 to 31/12/2007) at Calcutta Institute Of Technology.	
	• Research		
	• Industry		
	• Others		
vii)	Area of Specialization	Instrumentation & Control	
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. Electrical & Electronic Measurement.(Diploma Level) 2. Control System.(Under Graduate Level)	
ix)	Research guidance		
	• No. of papers published in National / International Journals/ Conferences	5 Nos (conferences)	
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		

i)	Name	SARASIJ ADHIKARY	
ii)	Date of Birth	16/02/1992	
iii)	Unique ID	1-3605755055	
iv)	Date of joining	15/03/2017	
v)	Education Qualification	M.TECH from W.B.U.T	
vi)	Work Experience		
	• Teaching	Works as an Assistant Professor at Calcutta Institute Of Technology	
	• Research		
	• Industry		
	• Others		
vii)	Area of Specialization	Power System	
viii)	Courses taught at Diploma/Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. Power System 2. Circuit Theory 3. Basic Electrical Engineering 4. Power Electronics	
ix)	Research guidance		
	• No. of papers published in National / International Journals/Conferences	1	
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


Dept-EE

i)	Name	Rakesh Das	
ii)	Date of Birth	23/09/1991	
iii)	Unique ID	1-3605685287	
iv)	Date of joining	01/08/2013	
v)	Education Qualification	B. Tech	
vi)	Work Experience		
	• Teaching	Works as. LECTURER at Calcutta Institute Of Technology	
	• Research		
	• Industry		
	• Others		
vii)	Area of Specialization		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	ET, CIRCUIT THEORY, NETWORK ANALYSIS, ELECTRICAL MACHINES FOR DETCE, SWITCHGEAR AND PROTECTION, ELECTRICAL MEASUREMENT AND CONTROL	
ix)	Research guidance		
	• No. of papers published in National / International Journals/Conferences	01	
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


Dept-EE

i)	Name	PRAKASH KUMAR SINGH	
ii)	Date of Birth	08/11/1993	
iii)	Unique ID	1-3605685027 (AICTE)	
iv)	Date of joining	09/08/2016	
v)	Education Qualification	B.TECH from MAKAUT	
vi)	Work Experience		
	• Teaching	Works as a Lecturer at Calcutta Institute Of Technology (09/08/2016 to continue)	
	• Research	NA	
	• Industry		
	• Others	NA	
vii)	Area of Specialization	Electrical Engineering	
viii)	Courses taught at Diploma/Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. Electrical Workshop (Diploma 1 st year & 3 rd year) 2. Professional Practice (Diploma 2 nd year & 3 rd year) 3. Power Plant Engineering (Diploma 2 nd year)	
ix)	Research guidance		
	• No. of papers published in National / International Journals/Conferences	NA	
	• Master		
	• Ph.D.	NA	
x)	Projects Carried out	NA	
xi)	Patents	NA	
xii)	Technology Transfer	NA	
xiii)	Research Publications	NA	
xiv)	No. of Books published with details	NA	


Dept-EE

i)	Name	SUJAN SIDDIQUEE	
ii)	Date of Birth	02/02/1994	
iii)	Unique ID	1-3605917231 (AICTE)	
iv)	Date of joining	16/08/2015	
v)	Education Qualification	B.TECH from MAKAUT	
vi)	Work Experience		
	• Teaching	WorksasaLectureratCalcuttaInstituteOfTechnology(17/08/2015tocontinue)	
	• Research		
	• Industry		
	• Others		
vii)	Area of Specialization	Electrical Engineering	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. Electrical Machine Lab 2. Electrical Measurement Lab 3. Electrical Drives Lab 4. Basic Electrical Lab. 5. Transmission & Distribution lab (Diploma 2 nd year) 6. Electrical Machine (Diploma 2 nd year)	
ix)	Research guidance		
	• No. of papers published in National / International Journals/Conferences		
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


Dept-EE


i)	Name	SUBHASREE CHATTERJEE	
ii)	Date of Birth	22/08/1994	
iii)	Unique ID	1-3605755453	
iv)	Date of joining	09/08/2016	
v)	Education Qualification	B TECH (MAKAUT) in Electrical Engineering	
vi)	Work Experience		
	• Teaching	Works as a Lecturer in Electrical Engineering Department at Calcutta Institute Of Technology since 09/08/16	
	• Research		
	• Industry		
	• Others		
vii)	Area of Specialization	Electrical Engineering	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate / Post Graduate Diploma Level		
ix)	Research guidance		
	• No. of papers published in National / International Journals/Conferences		
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		

Dept-EE


i)	Name	NILOY ROY	
ii)	Date of Birth	20/09/1991	
iii)	Unique ID		
iv)	Date of joining	11/08/2017	
v)	Education Qualification	DIPLOMA IN ELECTRICAL ENGINEERING FROM MURSHIDABAD INSTITUTE OF TECHNOLOGY UNDER WBSCTE.	
vi)	Work Experience	1YEAR,10 MONTHS	
	• Teaching	WORKS AS A TECHNICAL ASSISTANT AT CALCUTTA INSTITUTE OF TECHNOLOGY FROM 11 TH AUGUST'2017	
	• Research		
	• Industry	WORKED AS A JUNIOR FOREMAN (TECHNICIAN APPRENTICE) AT THE CALCUTTA TRAMWAYS COMPANY (1978)LIMITED FOR 1YEAR.	
	• Others		
vii)	Area of Specialization	ELECTRICAL ENGINEERING	
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level		
ix)	Research guidance		
	• No.ofpaperspublished in National / International Journals/ Conferences		
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		

Dept-EE


i)	Name	SURAJIT PRAMANICK	
ii)	Date of Birth	07/04/1995	
iii)	Unique ID		
iv)	Date of joining	11/08/2017	
v)	Education Qualification	Diploma in electrical engineering	
vi)	Work Experience		
	• Teaching	Works as a Technical Assistant at Calcutta Institute of Technology	
	• Research		
	• Industry		
	• Others		
vii)	Area of Specialization		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	Diploma in Electrical engineering (Industrial Control)	
ix)	Research guidance		
	• No. of papers published in National / International Journals/Conferences		
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		

i)	Name	DIPTAN SINHA	
ii)	Date of Birth	01/01/1993	
iii))	Unique ID	1-7430484060 (AICTE)	
iv))	Date of joining	06/08/2019	
v)	Education Qualification	M.TECH IN POWER SYSTEMS	
vi))	Work Experience	4.5	
	● Teaching	WORKS AT CALCUTTA INSTITUTE OF TECHNOLOGY	
	● Research	N/A	
	● Industry	N/A	
	● Others		
vi i)	Area of Specialization	ENERGY MANAGEMENT	
viii))	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. RENEWABLE & NON-CONVENTIONAL ENERGY/ENERGY AUDIT 2. CONTROL SYSTEM 3. UTILISATION OF ELECTRIC POWER 4. INDUSTRIAL ELECTRICAL MANAGEMENT 5. HVDC 6. BASIC ELECTRICAL	
ix))	Research guidance		
	● No. of papers published in National / International Journals/ Conferences	N/A	
	● Master		
	● Ph.D.		
x)	Projects Carried out		
xi))	Patents		
xi i)	Technology Transfer		
xi ii)	Research Publications		
xi v)	No. of Books published with details		


MECHANICAL ENGG DEPARTMENT

i)	Name	NIRVIK BANERJEE	
ii)	Date of Birth	09/10/1990	
iii)	Unique ID	1-3611944471 (AICTE)	
iv)	Date of joining	14/02/2017	
v)	Education Qualification	M.TECH IN AUTOMOBILE ENGINEERING, MCKVIE (MAKAUT)	
vi)	Work Experience		
	• Teaching	Works as Assistant Professor at Calcutta Institute Of Technology (2.5 YEARS)	
	• Research	NA	
	• Industry	VARROC EXCELLENCE , AURANGABAD (1 YEAR)	
	• Others	TEACHING ASSISTANTSHIP , MCKVIE (2 YEARS)	
vii)	Area of Specialization	Mechanical & Automobile	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. AUTOMOBILE ENGG, QUALITY & RELIABILITY 8 th SEMUG 2. IC ENGINE AND GAS TURBINE, 6 th SEMUG	
ix)	Research guidance	NA	
	• No. of papers published in National / International Journals/Conferences	NA	
	• Master	NA	
	• Ph.D.	NA	
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


MECHANICAL ENGG DEPARTMENT

i)	Name	AMIT RATAN BISWAS		
ii)	Date of Birth	26/09/1989		
iii)	Unique ID	1-3611671334 (AICTE)		
iv)	Date of joining	24/08/2016		
v)	Education Qualification	M.TECH IN MANUFACTURING TECHNOLOGY (NITTTR-KOL)		
vi)	Work Experience			
	• Teaching	Works as Assistant Professor at Calcutta Institute Of Technology (2 YEARS AND 10 MONTHS)		
	• Research	NA		
	• Industry	NA		
	• Others	NA		
vii)	Area of Specialization	MECHANICAL ENGINEERING (MANUFACTURING)		
viii)	Courses taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. WELDING TECHNOLOGY, ADVANCED MANUFACTURING 7 th SEMUG 2. DYNAMICS OF MACHINES, 5 th SEMUG 3. PRIMARY MFG. TECH., MECHANISMS 3 rd AND 4 th SEMUG		
ix)	Research guidance	NA		
	• No. of papers published in National / International Journals / Conferences	PAPER PUBLISHED-2 (INTERNATIONAL JOURNAL) CONFERENCE ATTENDED-NATIONAL (1), INTERNATIONAL (1)		
	• Master	NA		
	• Ph.D.	NA		
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications			
xiv)	No. of Books published with details			


MECHANICAL ENGGDEPARTMENT

i)	Name	DEBDIP CHAKRABORTY	
ii)	Date of Birth	20/08/1991	
iii)	Unique ID	1-361476781 (AICTE)	
iv)	Date of joining	16/02/2018	
v)	Education Qualification	M.TECH IN MECHANICAL ENGINEERING, HALDIA INSTITUTE OF TECHNOLOGY (MAKAUT)	
vi)	Work Experience		
	• Teaching	Works as Assistant Professor at Calcutta Institute Of Technology (1.4 YEARS)	
	• Research	NA	
	• Industry	Worked Assistant Engineer in Dynamic Engineers (6 months)	
	• Others		
vii)	Area of Specialization	Mechanical & Manufacturing Technology	
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. PRIMARY MANUFACTURING PROCESS,4 th SEMUG 2. MACHINING PRINCIPLES AND MACHINE TOOLS,6 th SEMUG	
ix)	Research guidance	NA	
	• No. of paperspublished in National / International Journals/ Conferences	NA	
	• Master	NA	
	• Ph.D.	NA	
x)	Projects Carried out	NA	
xi)	Patents	NA	
xii)	Technology Transfer	NA	
xiii)	Research Publications	NA	
xiv)	No. of Books published with details	NA	


MECHANICAL ENGG DEPARTMENT

i)	Name	UJJAL SAHU	
ii)	Date of Birth	30/11/1989	
iii)	Unique ID	1-3612035380 (AICTE)	
iv)	Date of joining	16/08/2017	
v)	Education Qualification	M.TECH IN AUTOMOBILE ENGINEERING, JADAVPUR UNIVERSITY	
vi)	Work Experience		
	• Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology (1YEAR 10 MONTHS)	
	• Research	NA	
	• Industry	NA	
	• Others	Works as a Lecturer at NIBEDITA POLYTECHNIC (2.5 YEARS)	
vii)	Area of Specialization	Mechanical & Automobile	
viii)	Courses taught at Diploma/Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. AUTOMOBILE ENGG, 8 th SEM UG 2. IC ENGINE AND GAS TURBINE, 6 th SEM UG 3. FLUID MECHANICS, 4 th SEM UG 4. APPLIED THERMODYNAMICS, 3 rd SEM UG	
ix)	Research guidance	NA	
	• No. of papers published in National / International Journals/Conferences	NA	
	• Master	NA	
	• Ph.D.	NA	
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


MECHANICAL ENGGDEPARTMENT

i)	Name	PARTHA SARATHI GHOSH	
ii)	Date of Birth	06/03/1991	
iii)	Unique ID	1-4731221613 (AICTE)	
iv)	Date of joining	09/08/2016	
v)	Education Qualification	M.TECH IN MANUFACTURING TECHNOLOGY (NITTTR-KOL)	
vi)	Work Experience		
	• Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology (2 YEARS AND 11 MONTHS)	
	• Research	2 YEARS	
	• Industry	NA	
	• Others	NA	
vii)	Area of Specialization	MANUFACTURING TECHNOLOGY	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. ADVANCED MANUFACTURING PROCESS 2. MACHINING PRINCIPLES AND MACHINE TOOLS 3. PRIMARY MANUFACTURING PROCESSES	
ix)	Research guidance	NA	
	• No. of papers published in National / International Journals/Conferences	2	
	• Master	NA	
	• Ph.D.	PURSUING FROM IIT DHANBAD	
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


MECHANICAL ENGG DEPARTMENT

i)	Name	DIPANJAN PAUL	
ii)	Date of Birth	05/02/1994	
iii)	Unique ID	1-4728888739	
iv)	Date of joining	27/09/2018	
v)	Education Qualification	M.TECH IN MECHANICAL SYSTEM DESIGNING AND INNOVATION TECHNOLOGY, NIT ARUNACHAL PRADESH.	
vi)	Work Experience		
	• Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology (1 YEARS)	
	• Research	NA	
	• Industry	NA	
	• Others	TEACHING ASSISTANTSHIP , NIT ARUNACHAL PRADESH (2 YEARS)	
vii)	Area of Specialization	Machine Design.	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. Machine design. 2. Dynamics of Machines. 3. Strength of Materials.	
ix)	Research guidance	NA	
	• No. of papers published in National / International Journals/Conferences	NA	
	• Master	NA	
	• Ph.D.	NA	
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


MECHANICAL ENGG DEPARTMENT

i)	Name	AVISHEK MUKHERJEE		
ii)	Date of Birth	27/05/1992		
iii)	Unique ID	1-3611940332 (AICTE)		
iv)	Date of joining	14/02/2017		
v)	Education Qualification	M.TECH in Mechanical Engineer from Haldia Institute of Technology(MAKAUT)		
vi)	Work Experience			
	• Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology (2.5 YEARS)		
	• Research	NA		
	• Industry	NA		
	• Others			
vii)	Area of Specialization	Mechanical & Manufacturing		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. Strength of Materials 2. Mechanisms 3. Dynamics of Machines 4. Machine design		
ix)	Research guidance	NA		
	• No. of papers published in National / International Journals/Conferences	One		
	• Master	One		
	• Ph.D.	NA		
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications			
xiv)	No. of Books published with details			


MECHANICAL ENGG DEPARTMENT

i)	Name	ABHISHEK SEN		
ii)	Date of Birth	22/09/1986		
iii)	Unique ID	1-4728888469		
iv)	Date of joining	01/12/2018		
v)	Education Qualification	PhD(Thesis Submitted), M.E., B.TECH		
vi)	Work Experience			
	• Teaching	3.2 Years		
	• Research	6.4 Years		
	• Industry	Nil		
	• Others	Nil		
vii)	Area of Specialization	Production Engineering		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. Materials Handling 2. Dynamics of Machines 3. Design of Machine Elements 4. Engineering Materials		
ix)	Research guidance	UG-2		
	• No. of papers published in National / International Journals/Conferences			
	• Master	Book-1		
	• Ph.D.	International Journal 1. Published:2 2. Accepted:4 3. Under Review:1 International Conferences-14 Book Chapters-3		
x)	Projects Carried out	NA		
xi)	Patents	NA		
xii)	Technology Transfer	NA		
xiii)	Research Publications	International Journal 1. Published:2 2. Accepted:4 3. Under Review: 1 International Conferences-14 Book Chapters-3		
xiv)	No. of Books published with details	Determination of the Enthalpy Changes of Chemical Reaction Using DTA, LAP Lambert Academic Publishing (3 August 2015), ISBN: 9783659748394.		


MECHANICAL ENGG DEPARTMENT


i)	Name	SUBHASRI ADHIKARI	
ii)	Date of Birth	29/01/1980	
iii)	Unique ID	1-4771852149 (AICTE)	
iv)	Date of joining	28/10/2014	
v)	Education Qualification	BME , Jadavpur University	
vi)	Work Experience		
	• Teaching	Works as a Lecturer at Calcutta Institute Of Technology (10 YEARS)	
	• Research	NA	
	• Industry	NA	
	• Others		
vii)	Area of Specialization	Mechanical	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	<ol style="list-style-type: none"> 1. Engineering Mechanics 2. Strength of Materials 3. Thermal Engineering I 4. Thermal Engineering II 5. Power Engineering 6. Refrigeration and Air conditioning 7. Design of Machine Elements 8. Fluid Mechanics and Hydraulic machines 	
ix)	Research guidance	NA	
	• No. of papers published in National / International Journals/Conferences		
	• Master		
	• Ph.D.	NA	
x)	Projects Carried out	NA	
xi)	Patents	NA	
xii)	Technology Transfer	NA	
xiii)	Research Publications	NA	
xiv)	No. of Books published with details	NA	

MECHANICAL ENGG DEPARTMENT


i)	Name	SURAJIT GHOROI		
ii)	Date of Birth	09/01/1970		
iii)	Unique ID	1-4774145240		
iv)	Date of joining	15/07/2014		
v)	Education Qualification	Diploma (Calcutta Technical School), A.M.I.E.		
vi)	Work Experience			
	• Teaching	Works as a Lecturer at Calcutta Institute Of Technology (08 YEARS)		
	• Research	NA		
	• Industry	22 Years		
	• Others			
vii)	Area of Specialization	Mechanical		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	<ol style="list-style-type: none"> 1. Manufacturing Process I 2. Manufacturing Process II 3. Engineering Metrology 4. Machine Drawing 5. Engineering Drawing 6. Workshop Practice I 7. Elements of Mechanical Engineering 8. Principles of Machines and Machine Tools 		
ix)	Research guidance	NA		
	• No. of papers published in National / International Journals/Conferences			
	• Master			
	• Ph.D.	NA		
x)	Projects Carried out	NA		
xi)	Patents	NA		
xii)	Technology Transfer	NA		
xiii)	Research Publications	NA		
xiv)	No. of Books published with details	NA		

MECHANICAL ENGGDEPARTMENT

i)	Name	GAUTAM MARIK	
ii)	Date of Birth	07/01/1974	
iii)	Unique ID		
iv)	Date of joining	1/09/2005	
v)	Education Qualification	BSC & DIPLOMA IN MECHANICAL ENGINEERING	
vi)	Work Experience		
	• Teaching	Works as a technical assistant in Calcutta Institute Of Technology. (1 ST September 2005 to Present)	
	• Research		
	• Industry	1 year in Calcutta state Transport & 7 years in Bengal brush product	
	• Others		
vii)	Area of Specialization	Drawing & SOM	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. technical drawing (Diploma 1 st yr 1 st & 2 nd Sem) 2. SOM (Diploma 1 st yr 2 nd sem) 3. Engineering Mechanics (Diploma 1 st yr 1 st sem) 4. Engineering Drawing & graphics (Btech 1 st yr 2 nd sem) 5. SOM Lab (Btech 2 nd sem & 8 th sem)	
ix)	Research guidance		
	• No. of papers published in National / International Journals/Conferences		
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		

i)	Name	DR. SAURABH ACHARYYA	
ii)	Date of Birth	13/05/1977	
iii)	Unique ID	1-506074063	
iv)	Date of joining	11/08/2010	
v)	Education Qualification	Ph.D. (University of Calcutta)	
vi)	Email Id	saurabh_acharyya@rediffmail.com	
vii)	Work Experience	1. Teaching Experience: 11 Yrs ; 2. Industrial Experience: 2 Yrs	
	• Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology (2010 to continue)	
	• Research	10 - YEARS	
	• Industry	N/A	
	• Others	N/A	
viii)	Area of Specialization	Organic Chemistry	
ix)	Courses taught at Diploma/ B.Tech Level	11. Organic Chemistry; 2. Chemistry of Heterocyclic; 3. Organic Synthesis; 4. Disconnection Approach; 5. Thermodynamics; 6. Reaction dynamics; 7. Electrochemistry; 8. Environmental and Ecology 9. Natural products	
x)	Research guidance	NIL	
	• No. of papers published in National / International Journals/Conferences	4 (Four)	
	• Master		
	• Ph.D.	N/A	
xi)	Projects Carried out	10 (UG Level)	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	4	
xv)	No. of Books published with details	N/A	


Dept- Gen. Sc

i)	Name	TAPAS KUMAR GHOSH	
ii)	Date of Birth	03/05/1984	
iii)	Unique ID	1-506103859	
iv)	Date of joining	03/08/2010	
v)	Education Qualification	B.Sc.(Chemistry), M.Sc.(Organic Chemistry),Ph.D(Chemistry)	
vi)	Email Id	tghoshmail.com@gmail.com	
vii)	Work Experience	2. Teaching Experience: 9 Yrs ; 2. IndustrialExperience:1Yr 3. Research Experience:8Yrs	
	• Teaching	9 - Yrs (Assistant Professor from DOJ:)	
	• Research	N/A	
	• Industry	N/A	
	• Others	N/A	
viii)	Area of Specialization	Polymercompositesandtheirapplications,GraphemeSynthesis,Dye Degradation study, GreenChemistry	
ix)	CoursetaughtatDiploma/ B.TechLevel	12. 1AppliedChemistry-1 13. AppliedChemistry-2 14. EngineeringChemistry 15. BasicEnvironmentalEngineering 16. PolymerChemistry 17. Water pollution andcontrol	
x)	Research guidance	NIL	
	• No.ofpaperspublished in National / International Journals/ Conferences	7 / 7	
	• Master	N/A	
	• Ph.D.	N/A	
xi)	Projects Carried out	20 (UG Level) ; 1 (PG Level)	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	N/A	
xv)	No. of Books published with details	N/A	


Dept- Gen. Sc

i)	Name	TULI BAKSHI	
ii)	Date of Birth	04/04/1977	
iii)	Unique ID	1-488252521	
iv)	Date of joining	12/01/2005	
v)	Education Qualification	PhD(Engg.), M.Tech (IT), MCA, M.Sc.(Applied Maths)	
vi)	Email Id	tuli.bakshi@gmail.com	
vii)	Work Experience	3. Teaching Experience: 15 yrs ; 2. Research Experience: 5 yrs	
	• Teaching	15 - Yrs (Assistant Professor from DOJ)	
	• Research	5 - Yrs	
	• Industry	N/A	
	• Others	N/A	
viii)	Area of Specialization	Mathematics	
ix)	Courses taught at Diploma/B.Tech Level	18. 1. Operation researchs 19. Numerical Analysis 20. Mathematics 21. Artificial Intelligence 22. Discrete Mathematical Structure 23. Software Engineering 24. Database Management Technique 25. Soft Computing	
x)	Research guidance	NIL	
	• No. of papers published in National / International Journals/Conferences	0 /10 /25	
	• Master	N/A	
	• Ph.D.	N/A	
xi)	Projects Carried out	N/A	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	N/A	
xv)	No. of Books published with details	N/A	


Dept- Gen. Sc

i)	Name	KRISHNENDU PRAMANIK	
ii)	Date of Birth	13/05/1984	
iii)	Unique ID	1-506103863	
iv)	Date of joining	01/02/2008	
v)	Education Qualification	Ph.D. (University of Calcutta)	
vi)	Email Id	krishnendu_chem@yahoo.com	
vii)	Work Experience	1. Teaching Experience: 11Yrs ; 2. Industrial Experience:1-Yr	
	• Teaching	WrksasaAssistantProfessoratCalcuttaInstituteOfTechnology(01/02/2008 tocontinue)	
	• Research	6- Yrs	
	• Industry	1-Yr	
	• Others	N/A	
viii)	Area of Specialization	Organic Synthesis	
ix)	CoursetaughtatDiploma/B.Tech Level	26. Organic Chemistry 2. Chemistry of Heterocyclic 2. Organic Synthesis 4. DisconnectionApproach, 5. Thermodynamics, 6. Reaction dynamics 7. Quantum Mechanics 8. Electrochemistry 9. Environmental and Ecology	
x)	Research guidance	NI	
	• No. of papers published in National / International Journals/Conferences	4 (Four)	
	• Master	N/A	
	• Ph.D.	N/A	
xi)	Projects Carried out	1(One) – SERB ; 10 (UG Level)	
xii)	Patents	1 (One)	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	4 (Four)	
xv)	No. of Books published with details	N/A	


Dept- Gen. Sc

i)	Name	Mamata Paul		
ii)	Date of Birth	29/07/1991		
iii)	Unique ID	1-9324467585		
iv)	Date of joining	09/08/2017		
v)	Education Qualification	M.A.(English)		
vi)	Email Id	29mamatapaul@gmail.com		
vii)	Work Experience			
	• Teaching	1.5 Yrs		
	• Research	N/A		
	• Industry	N/A		
	• Others	N/A		
viii)	Area of Specialization			
ix)	Courses taught at Diploma/B.Tech Level	27. Lifeskills 28. Softskills 29. Technical and business communications 30. Humanities and Social Science		
x)	Research guidance	NIL		
	• No. of papers published in National / International Journals/Conferences	N/A		
	• Master			
	• Ph.D.	N/A		
xi)	Projects Carried out			
xii)	Patents	N/A		
xiii)	Technology Transfer	N/A		
xiv)	Research Publications	N/A		
xv)	No. of Books published with details	N/A		


Dept- Gen. Sc

i)	Name	PALASH SAHOO		
ii)	Date of Birth	10/02/1984		
iii)	Unique ID	1-1507854639		
iv)	Date of joining	07/03/2014		
v)	Education Qualification	B.SC (H) in Mathematics (Vidyasagar University),M.SC in Mathematics (NIT, Rourkela), PhD(Pursuing)		
vi)	Email Id	palashsahoo86@gmail.com		
vii)	Work Experience	4. Teaching Experience: 5 Yrs ; 2. Research Experience: 2.5 Yrs		
	• Teaching	5 - Yrs (Assistant Professor from DOJ: 07/03/2014)		
	• Research	N/A		
	• Industry	N/A		
	• Others	N/A		
viii)	Area of Specialization	Mathematics		
ix)	Courses taught at Diploma/B.Tech Level	31. 1. Ordinary Differential Equation 32. Theory of Probability 33. Numerical Methods 34. Operation Research		
x)	Research guidance	NIL		
	• No. of papers published in National / International Journals/Conferences	2 (Two)		
	• Master	N/A		
	• Ph.D.	N/A		
xi)	Projects Carried out	N/A		
xii)	Patents	N/A		
xiii)	Technology Transfer	N/A		
xiv)	Research Publications	N/A		
xv)	No. of Books published with details	N/A		


Dept- Gen. Sc

i)	Name	SANTANU BASU	
ii)	Date of Birth	24/10/1967	
iii)	Unique ID	1-3600947482	
iv)	Date of joining	01/08/2013	
v)	Education Qualification	M.Sc.(Physics), M.Sc.(Maths), B.Ed., M.Ed., B.O.A.T. (4-Yrs DME & DEE)	
vi)	Email Id	stbasu@rediffmail.com	
vii)	Work Experience	5. Teaching Experience: 11 Yrs ; Industrial Experience: 18 Yrs 10 Mths	
	• Teaching	6-Yrs(AssistantProfessorfromDOJ:01/08/2013)	
	• Research	N/A	
	• Industry	18 Yrs 10 Mths	
	• Others	N/A	
viii)	Area of Specialization	Condensed Matter Physics	
ix)	CoursetaughtatDiploma/B.Tech Level	35. Basic Engg Physics & Integrated Physics-B.Tech. 36. Basic Physics & Applied Physics –Diploma	
x)	Research guidance	NIL	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	N/A	
	• Ph.D.	N/A	
xi)	Projects Carried out	N/A	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	N/A	
xv)	No. of Books published with details	N/A	


Dept- Gen. Sc

i)	Name	Mr. SANTANU BHATTACHARJEE		
ii)	Date of Birth	01/05/1987		
iii)	Unique ID	1-507271297		
iv)	Date of joining	20/02/2010		
v)	Education Qualification	M.Sc.(Physics) Guru Ghasidas University, B.Sc. (Physics) Vidyasagar university		
vi)	Email Id	santanucit10@gmail.com		
vii)	Work Experience			
	<ul style="list-style-type: none">Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology & AHS (20/02/2010 to continue)		
	<ul style="list-style-type: none">Research	1 YEAR		
	<ul style="list-style-type: none">Industry	N/A		
	<ul style="list-style-type: none">Others	N/A		
viii)	Area of Specialization	Electronics, Quantum mechanics, Statistical Mechanics, General properties of matter		
ix)	Courses taught at Diploma/ B.Tech Level	1. Basic Physics (Diploma 1 st year) 2. Physics-I, Physics-II(B.Tech 1 st year and 2 nd year)		
x)	Research guidance	NIL		
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences	N/A		
	<ul style="list-style-type: none">Master	N/ A		
	<ul style="list-style-type: none">Ph.D.	N/A		
xi)	Projects Carried out			
xii)	Patents	N/A		
xiii)	Technology Transfer	N/A		
xiv)	Research Publications	N/A		
xv)	No. of Books published with details	N/A		


Dept- Gen. Sc

i)	Name	SAURAV MUKHERJEE	
ii)	Date of Birth	17/01/1990	
iii)	Unique ID	1-7487251348	
iv)	Date of joining	01/03/2019	
v)	Education Qualification	MBA(Major-Finance Minor- Marketing), CA Inter Qualified	
vi)	Email Id	sauravmukherjee87@gmail.com	
vii)	Work Experience	1.5 years corporate experience of working with PwC SDC as an audit consultant.	
	<ul style="list-style-type: none">Teaching	N/A	
	<ul style="list-style-type: none">Research	N/A	
	<ul style="list-style-type: none">Industry	1.5 years corporate experience of working with PwC SDC as an audit consultant.	
	<ul style="list-style-type: none">Others	N/A	
viii)	Area of Specialization	Management Studies, Financial Management, Accountancy, OrganisationalBehaviour	
ix)	Course taught at Diploma / B.Tech Level	1. Business Economics And Accountancy(forDiploma) 2. Economics forengineers(HU-501) 3. Principle ofmanagement(HU-601) 4. Organisationalbehavior(HU-801) 5. Grooming andpersonalitydevelopment	
x)	Research guidance	NIL	
	<ul style="list-style-type: none">No.ofpaperspublishedinNational/ InternationalJournals/Conferences	N/A	
	<ul style="list-style-type: none">Master	N/A	
	<ul style="list-style-type: none">Ph.D.	N/A	
xi)	Projects Carried out	N/A	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	N/A	
xv)	No. of Books published with details	N/A	


Dept- Gen. Sc


i)	Name	SAYANI DAS	
ii)	Date of Birth	31/10/1985	
iii)	Unique ID	1-3600766212	
iv)	Date of joining	15/07/2014	
v)	Education Qualification	M.Com(Accounts and Finance), MBA(Major-Finance Minor- Marketing)	
vi)	Email Id	mesayanidas@gmail.com	
vii)	Work Experience	1. Teaching Experience: 5 YEARS 7 MONTHS	
	• Teaching	5-Yrs(AssistantProfessorfromDOJ:15/07/2014)	
	• Research	N/A	
	• Industry	N/A	
	• Others	N/A	
viii)	Area of Specialization	COST ACCOUNTANCY	
ix)	CoursetaughtatDiploma/B.Tech Level	1. Values andethics(HU-301,HU-401) 2. Economics forengineers(HU-501) 3. Principle ofmanagement(HU-601) 4. Organisationalbehavior(HU-801) 5. ManagementAccounting(MBA-302) 6. Business Economics And Accountancy(forDiploma)	
x)	Research guidance	NIL	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	N/A	
	• Ph.D.	N/A	
xi)	Projects Carried out	N/A	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	N/A	
xv)	No. of Books published with details	N/A	

Dept- Gen. Sc

i)	Name	SOUBHIK BURAI	
ii)	Date of Birth	06/05/1988	
iii)	Unique ID	1-3600649350	
iv)	Date of joining	27/02/2015	
v)	Education Qualification	M.A in English (W.B.S.U), B.Ed (C.U)	
vi)	Email Id	soubhikburai88@gmail.com	
vii)	Work Experience	2. Teaching Experience: 6 YEARS	
	• Teaching	5-Yrs(AssistantProfessorfromDOJ:15/07/2014)	
	• Research	N/A	
	• Industry	N/A	
	• Others	N/A	
viii)	Area of Specialization	English	
ix)	Courses taught at Diploma/B.Tech Level	<ol style="list-style-type: none"> 1. Business Communicative English 2. Technical Communication 3. Development of Soft Skills 4. Development of Life Skills 	
x)	Research guidance	NIL	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	N/A	
	• Ph.D.	N/A	
xi)	Projects Carried out	N/A	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	N/A	
xv)	No. of Books published with details	N/A	

Dept- Gen. Sc


i)	Name	SOUMITA GHOSH	
ii)	Date of Birth	15/07/1985	
iii)	Unique ID	1-3761390294	
iv)	Date of joining	17/08/2010	
v)	Education Qualification	M.Sc.(Physics)	
vi)	Email Id	ghosh.soumita15@gmail.com	
vii)	Work Experience	3. Teaching Experience: 8-YEARS 6-MONTHS	
	• Teaching	8-YEARS 6-MONTHS (Assistant Professor from DOJ)	
	• Research	N/A	
	• Industry	N/A	
	• Others	N/A	
viii)	Area of Specialization	SOLID STATE PHYSICS	
ix)	Courses taught at Diploma/B.Tech Level	37. OPTICS 38. VECTOR CALCULUS 39. LASER 40. STATISTICAL MECHANICS 41. SEMICONDUCTOR & X-RAY 42. CLASSICAL MECHANICS 43. ELECTROMAGNETISM 44. CRYSTAL STRUCTURE 45. SOLID STATE PHYSICS	
x)	Research guidance	NIL	
	• No. of papers published in National / International Journals/Conferences	N/A	
	• Master	N/A	
	• Ph.D.	N/A	
xi)	Projects Carried out	N/A	
xii)	Patents	N/A	
xiii)	Technology Transfer	N/A	
xiv)	Research Publications	N/A	
xv)	No. of Books published with details	N/A	

i)	Name	Avishek Mondal	 IMAGE
ii)	Date of Birth	02/12/1991	
iii)	Unique ID	20210014	
iv)	Date of joining	07/04/2021	
v)	Education Qualification	M.Sc	
vi)	Work Experience	1year	
	<ul style="list-style-type: none"> Teaching 		
	<ul style="list-style-type: none"> Research 		
	<ul style="list-style-type: none"> Industry 		
	<ul style="list-style-type: none"> Others 		
vii)	Area of Specialization		
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	3. Mathematics	
ix)	Research guidance		
	<ul style="list-style-type: none"> No. of papers published in National / International Journals/ Conferences 		
	<ul style="list-style-type: none"> Master 		
	<ul style="list-style-type: none"> Ph.D. 		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		

Dept- Gen. Sc

i)	Name	Kuheli Bhowmick		
ii)	Date of Birth	09/10/1993		
iii)	Unique ID	20200010		
iv)	Date of joining	21/02/2020		
v)	Education Qualification	M.sc, B.ed		
vi)	Work Experience			
	• Teaching	Almost 2 years		
	• Research	NA		
	• Industry	NA		
	• Others	Did workshop on bioinformatics, plant tissue culture		
vii)	Area of Specialization	Cytogenetics and plant biotechnology		
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. Under graduate		
ix)	Research guidance	NA		
	• No. of papers published in National / International Journals/ Conferences	NA		
	• Master	M.sc in Botany		
	• Ph.D.	NA		
x)	Projects Carried out	NA		
xi)	Patents	NA		
xii)	Technology Transfer	NA		
xiii)	Research Publications	NA		
xiv)	No. of Books published with details	NA		


Dept-MCA

Name	BIPLAB KANTI DAS	
Date of Birth	03/08/1979	
Unique ID	1-488252529(AICTE)	
Date of joining	15/01/2008	
Education Qualification	M.Tech (IT) from JADAVPUR UNIVERSITY	
Work Experience		
<ul style="list-style-type: none"> Teaching 	<p>Working as a Assistant Professor at Calcutta Institute Of Technology since Jan' 2008</p> <p>Worked as a Faculty member at Institute of Computer Engineers (India) a sister concern of Techno India Group from Jan'1998 to Aug' 2005.</p>	
<ul style="list-style-type: none"> Research 	4 years	
<ul style="list-style-type: none"> Industry 		
<ul style="list-style-type: none"> Others 		
Area of Specialization	Medical image processing	
Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. C Programming, Computer Fundamental (Diploma 2 nd & 1 st year) 2. Computer Graphics, OOPS, c programming, Operating System (MCA-1 st , 2 nd year)	
Research guidance		
<ul style="list-style-type: none"> No. of papers published in National / International Journals/Conferences 	3	
<ul style="list-style-type: none"> Master 		
<ul style="list-style-type: none"> Ph.D. 		
Projects Carried out		
Patents		
Technology Transfer		
Research Publications	3	
No. of Books published with details	1) A Framework of Intelligent Tutorial System Paperback, Published by LAMBERT Academic Publishing 2) 3 School books on COMPUTER LEARNING for class V, VI, VIII	


Dept-MCA

i)	Name	CHANCHAL GHOSH		
ii)	Date of Birth	12/11/1980		
iii)	Unique ID	1-488252533 (AICTE)		
iv)	Date of joining	16/08/2006		
v)	Education Qualification	MCA from Jadavpur University MTech (IT) from Jadavpur University		
vi)	Work Experience			
	• Teaching	Works as a Assistant Professor at Calcutta Institute Of Technology (16/08/2006 to continue)		
	• Research			
	• Industry	6 Months in 4 future engineers		
	• Others			
vii)	Area of Specialization	Information Security		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. programming for problems solving (CHE 1 st year 2 nd sem) 2. Discrete Mathematics (MCA 1 st year 1 st sem) 3. Data structure using C (MCA 1 st year 2 nd sem) 4. Artificial Intelligence (MCA 2 nd year 1 st sem) 5. Operation Research and optimization technique (MCA 2 nd year 2 nd sem) 6. C programming (DETCE 2 nd year 1 st sem)		
ix)	Research guidance			
	• No. of papers published in National / International Journals/Conferences			
	• Master			
	• Ph.D.			
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications	1		
xiv)	No. of Books published with details			

Dept-MCA

i)	Name	SUBRATA JANA		
ii)	Date of Birth	02-01-1978		
iii)	Unique ID	1-3612458751		
iv)	Date of joining	17/08/2006		
v)	Education Qualification	MCA,M.Tech(CS)		
vi)	Work Experience			
	• Teaching	12 YEARS		
	• Research			
	• Industry	11 MONTH		
	• Others			
vii)	Area of Specialization	BIOMEDICAL IMAGE PROCESSING		
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1.DIPLOMA 2.B.TECH 3.MCA		
ix)	Research guidance			
	• No.ofpaperspublished in National / International Journals / Conferences	3		
	• Master			
	• Ph.D.			
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications			
xiv)	No. of Books published with details			


Dept-MCA

i)	Name	JOYDEB SHEET	
ii)	Date of Birth	31/01/1992	
iii)	Unique ID	1-3612239273	
iv)	Date of joining	09/03/2017	
v)	Education Qualification	MCA	
vi)	Work Experience		
	• Teaching	<p>Worked as a Computer Faculty in Swarnalata Computer Training Center (Brainwaves Academy) from 01.09.2015 to 28.02.2017.</p> <p>Works as a Assistant Professor at Calcutta Institute Of Technology from 09/03/2017 to continue)</p>	
	• Research	NONE	
	• Industry	NONE	
	• Others	NONE	
vii)	Area of Specialization	Pharamaconosy and Phytochemistry	
viii)	CoursetaughtatDiploma/Post Diploma / Under Graduate / Post Graduate / Post Graduate DiplomaLevel	1.Diploma in Advanced Software Development from CMC	
ix)	Research guidance	NONE	
	• No.ofpaperspublishedin National / International Journals/Conferences	NO	
	• Master	MCA	
	• Ph.D.	NO	
x)	Projects Carried out		
xi)	Patents	NO	
xii)	Technology Transfer	NO	
xiii)	Research Publications	NO	
xiv)	No. of Books published with details	NO	

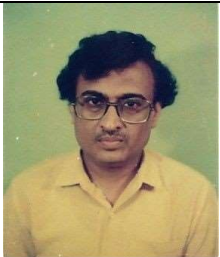
Faculty (Dept- CE)

i)	Name	Subhankar Nag		
ii)	Date of Birth	08/11/1965		
iii)	Unique ID	PAN Card		
iv)	Date of joining	01/08/2019		
v)	Education Qualification	B.E.-Civil (BEC/IEST, Shibpur); M. Tech. (IIT, Kharagpur); SMP (IIM, Calcutta); Fellow (Institution of Engineers, India); Chartered Engineer		
vi)	Work Experience			
	• Teaching	3 years		
	• Research	2 years		
	• Industry	28 years		
	• Others	NA		
vii)	Area of Specialization	Environmental Engineering, Construction Engineering Management, Project Planning		
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	4. Diploma – Environmental Engineering 5. UG – Environmental Engineering 6. UG – Construction Engineering Management 7. UG – Civil Engineering – Societal & Global Impact 8. UG – Introduction to Civil Engineering		
ix)	Research guidance	NA		
	• No. of papers published in National / International Journals/ Conferences	3 no. Papers published in International Research Journals 2 nos. Papers published in International Conferences held at Atlanta, USA and IIT, Kharagpur		
	• Master			
	• Ph.D.			
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer	Transfer of Environment friendly Low Cost Construction Technology options from Lab to Land under HUDCO, Govt. of India		
xiii)	Research Publications	3 no. Papers published in International Research Journals		
xiv)	No. of Books published with details			


Faculty (Dept- CE)

i)	Name	BIKRAM PAUL		
ii)	Date of Birth	14/07/1992		
iii)	Unique ID	1-11046028211		
iv)	Date of joining	8/12/2018		
v)	Education Qualification	ME (STRUCTURAL ENGINEERING) from Jadavpur University		
vi)	Work Experience			
	• Teaching	Works as an Assistant Professor at Calcutta Institute Of Technology (8/12/2018 to continue)		
	• Research			
	• Industry	Worked as a Site Engineer in UECS Ltd. from june 2014 to nov 2018		
	• Others			
vii)	Area of Specialization	Repair and Retrofit Engineering		
viii)	Courses taught at Diploma/Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. Structural Analysis/Solid Mechanics/Estimation and Costing (B.Tech 2nd year) 2. Construction Planning and Management/ Concrete Technology (B. Tech 3rd Year)		
ix)	Research guidance			
	• No. of papers published in National / International Journals/Conferences	3 No.		
	• Master			
	• Ph.D.			
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications	1 Journal		
xiv)	No. of Books published with details			


Faculty (Dept- CE)

i)	Name	JAYANTA BANERJEE	
ii)	Date of Birth	03/10/50	
iii)	Unique ID	1-46448010887	
iv)	Date of joining	13/02/2018	
v)	Education Qualification	B.E (CIVIL), M.Sc (STRUC) ,MBA	
vi)	Work Experience		
	<ul style="list-style-type: none">Teaching	Seven (7) years in graduate level	
	<ul style="list-style-type: none">Research	NA	
	<ul style="list-style-type: none">Industry	Forty (40) years in consulting firm	
	<ul style="list-style-type: none">Others	NA	
vii)	Area of Specialization	Structural Engineering	
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1) Solid Mechanics 2) Analysis Of Structure 2) Design of R.C. Struc 4) Design Of Steel Struc 5) Concrete Technology 6) Hydraulic Structure 7) Construction Planning & Mgmt 8) Transportation Engg	
ix)	Research guidance	NA	
	<ul style="list-style-type: none">No. of papers published in National / International Journals/Conferences		
	<ul style="list-style-type: none">Master		
	<ul style="list-style-type: none">Ph.D.		
x)	Projects Carried out	Vizag Steel Plant, Rourkela Steel Plant, TATA Steel, Bokaro Steel	
xi)	Patents	NA	
xii)	Technology Transfer	NA	
xiii)	Research Publications	NA	
xiv)	No. of Books published with details	NA	


Faculty (Dept- CE)

i)	Name	PROSENJIT DAS		
ii)	Date of Birth	02/09/1984		
iii)	Unique ID	1-1507761559		
iv)	Date of joining	02/08/2012		
v)	Education Qualification	M.E(Geotechnical Engineering) from BESU .		
vi)	Work Experience	10 Years		
	• Teaching	WorksasaAssistantProfessoratCalcuttaInstituteOfTechnology(02/08/2019to continue)		
	• Research			
	• Industry	3 Years as a Designer(Simplex Infrastructure Ltd , HRF Consultancy)		
	• Others	CHARTEREDENGINEER,APPROVEDVALUER,MIGS,MIRC,MISCA,MIPHE,AMIE, AIV.		
vii)	Area of Specialization	Geotechnical Engg.		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	1. SoilMechanics,BuildingMaterial&Construction (D.CE 2 nd &3 rd year) 2. SoilMechanics,BuildingMaterial&Construction,FoundationEngineering,Soil Stabilization&GroundImprovementTechniques.(B.Tech2 nd year,3 rd year&4 th Year)		
ix)	Research guidance			
	• No. of papers published in National / International Journals/Conferences			
	• Master			
	• Ph.D.			
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications			
xiv)	No. of Books published with details			


Faculty (Dept- CE)

i)	Name	RANAK PROSAD BANERJEE		
ii)	Date of Birth	12/12/1991		
iii)	Unique ID			
iv)	Date of joining	10/08/2015		
v)	Education Qualification	DIPLOMA IN SURVEY ENGINEERING		
vi)	Work Experience			
	<ul style="list-style-type: none"> Teaching 	<p>Works as a technical assistant in Calcutta Institute Of Technology. (10th August 2015 to Present)</p> <p>Works as an instructor in JLD PVTITI. (13th September 2012 to 9th August 2015)</p>		
	<ul style="list-style-type: none"> Research 			
	<ul style="list-style-type: none"> Industry 			
	<ul style="list-style-type: none"> Others 			
vii)	Area of Specialization	SURVEY & AUTOCAD		
viii)	Courses taught at Diploma/Post Diploma/Under Graduate/Post Graduate/Post Graduate Diploma Level	<p>1. Basic Surveying (Diploma 2nd yr 3rd sem & 4th sem) 2. Survey practical (Diploma 2nd yr 4th sem & 3rd yr 6th sem)</p> <p>3. AutoCad (Diploma 2nd yr 4th sem & 3rd yr 5th sem)</p> <p>4. Surveying (Theory & Lab) (Btech 2nd yr 3rd sem)</p> <p>5. Survey practical (Btech 2nd yr 3rd sem & 4th sem)</p> <p>6. AutoCad Design (Btech 3rd yr 4th sem)</p>		
ix)	Research guidance			
	<ul style="list-style-type: none"> No. of papers published in National / International Journals/Conferences 			
	<ul style="list-style-type: none"> Master 			
	<ul style="list-style-type: none"> Ph.D. 			
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications			
xiv)	No. of Books published with details			


Faculty (Dept- CE)

i)	Name	SUBHA SASMAL		
ii)	Date of Birth	14/05/1995		
iii)	Unique ID			
iv)	Date of joining	01/12/2018		
v)	Education Qualification	B.TEC H IN CIVIL ENGINEERING FROM MAKAUT		
vi)	Work Experience			
	<ul style="list-style-type: none"> Teaching 	<p>WORKEDASAGUESTFACULTYINCIVILENGINEERINGDEPT.OFNARULA INSTITUTE OF TECHNOLOGY FROM 26.07.2016 TO30.01.2018</p> <p>NOWWORKINGASALECTURERINCIVILENGINEERINGDEPT.OFCALCUTTA INSTITUTE OF TECHNOLOGY FROM01.012.2018</p>		
	<ul style="list-style-type: none"> Research 			
	<ul style="list-style-type: none"> Industry 			
	<ul style="list-style-type: none"> Others 			
vii)	Area of Specialization			
viii)	CoursetaughtatDiploma/Post Diploma/UnderGraduate/Post Graduate/PostGraduateDiploma Level			
ix)	Research guidance			
	<ul style="list-style-type: none"> No. of papers published in National / International Journals/Conferences 			
	<ul style="list-style-type: none"> Master 			
	<ul style="list-style-type: none"> Ph.D. 			
x)	Projects Carried out			
xi)	Patents			
xii)	Technology Transfer			
xiii)	Research Publications			
xiv)	No. of Books published with details			


Faculty (Dept- CE)

i)	Name	SUMANA MAITY	
ii)	Date of Birth	23/01/1994	
iii)	Unique ID	1-4645269826	
iv)	Date of joining	01/12/2018	
v)	Education Qualification	B.Tech(Civil Engineering) from Jalpaiguri Government Engineering College	
vi)	Work Experience		
	• Teaching	Works as a Lecturer at Calcutta Institute of Pharmaceutical Technology & AHS (01/12/2018 to continue)	
	• Research		
	• Industry		
	• Others		
vii)	Area of Specialization		
viii)	Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. Autocad 2. Abacus	
ix)	Research guidance		
	• No. of papers published in National / International Journals / Conferences		
	• Master		
	• Ph.D.		
x)	Projects Carried out		
xi)	Patents		
xii)	Technology Transfer		
xiii)	Research Publications		
xiv)	No. of Books published with details		


Dept- CHE

Name	Arunima Adak	
Unique ID	1-3619103911	
Department	Department of Chemical Engineering.	
Date of joining	15.02.2018	
Education Qualification	M. E. in Chemical Engineering from Jadavpur University, Kolkata, West Bengal.	
Work Experience	Working as an Assistant Professor in Department of Chemical Engineering.	
Area of Research	Mass Transfer.	
Seminar Attended	Attended seminars in various academic institutions.	
Subjects taught	<ul style="list-style-type: none">• ChemicalReactionEngineering.• ProcessControl.• EngineeringMathematics.• ChemicalengineeringThermodynamics.	


Dept- CHE

Name	DR.HARI SADHAN SARKAR	
Date of Birth	04.01.1944	
Unique ID	1-505933457	
Date of joining	02/04/2009	
Education Qualification	M.Tech., Ph.D. (IIT, Kharagpur)	
Work Experience		
<ul style="list-style-type: none">Teaching	12yearsasFacultyinNPTI/NITDurgapurandasProfessorofCalcuttaInstituteofTechnology,Uluberia, Howrah, India	
<ul style="list-style-type: none">Research	(i)04Yrs.atPlanning&DevelopmentDivision,F.C.I.,Sindri,Govt.OfIndia (ii)04 yrs. at DCL,Durgapur (iii))4 Yrs. at DPL, Durgapur	
<ul style="list-style-type: none">Industry	35Yrs.inFertilizer,HeavyChemical,Coal,Coke,CoalChemicals,Power,Water,CBMExploration and Production,etc.,	
<ul style="list-style-type: none">Others	Patent works(under process): 1) InulinManufactureandPurificationTechnologywithIndian Vegetables 2) High pressure catalytic hydrogenation of coal for motorfuelproduction 3) Algal growth throughGHGtransformation	
Area of Specialization	Coal Chemicals, High Pressure Catalysis, Advanced Environmental Pollution control Technology, etc.	
Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma Level	1. Chemical ProcessTechnology. 2. EnvironmentalEngineering. 3. Industrial SafetyEngineering.. 4. Project Management. 5. ProjectEngineering. 6. AdvanceSeparation	
Research guidance		
<ul style="list-style-type: none">No.ofpaperspublished in National / International Journals/ Conferences	04 papers published in International Journal of repute 08 papers published in National Journal	
<ul style="list-style-type: none">Master	10 PG students guided for their project works	
<ul style="list-style-type: none">Ph.D.	Guided for 03 nos. of Ph. D. students	
Projects Carried out	1. AdvancedtreatmenttechnologyonwastewaterfromRayon,RubberChemicalIndustries, FertilizerIndustries 2. GuidedIndustrialprojectsofCoal/Biomassgasification 3.Coalliquefaction	
Patents	03 Patents under process for international registration	
Technology Transfer	04 Nos. in FCI, DCL, DPL, Rubber Chemicals plants.	
Research Publications	04 Nos. in International Journals 08 Nos. in National Journals	
No. of Books published with details	01 Book is under process of publication	


Dept- CHE

Name	DebduttaMandal	
Unique ID	1-3619104045	
Department	Department of Chemical Engineering.	
Date of joining	13.02.2018	
Education Qualification	Masters of Technology (M.Tech) in Chemical Engineering from Calcutta University.	
Work Experience	Working as an Assistant Professor in Department of Chemical Engineering.	
Area of Research	Removal of water pollutants from wastewater through adsorption.	
Seminar Attended	Attended seminars in various academic institutions.	
Subjects taught	<ul style="list-style-type: none">• Engineeringchemistry.• Fluid mechanics• Processheattransfer.• Separation process IandII.	


Dept- CHE

Name	Dr. MonalDutta	
Unique ID	1-3601377814	
Department	Department of Chemical Engineering.	
Date of joining	01.08.2013	
Education Qualification	Doctor of Philosophy (PhD) in Chemical Engineering from Indian Institute of Technology, Kharagpur, India.	
Work Experience	Working as an Assistant Professor in Department of Chemical Engineering.	
Area of Research	<ul style="list-style-type: none">• Removal of water pollutants from wastewaterthroughadsorption.• Modeling ofadsorptionprocess.• MembraneSeparation.	
Publications	Published more than 20 papers in various peer reviewed International and National Journals and have several International and National conference proceedings.	
Subjects taught	<ul style="list-style-type: none">• IndustrialStiochiometry.• ProcessHeatTransfer.• MechanicalOperation.• Separation process IandII.	
Positions held	The Single Point of Contact (SPOC) of NPTEL.	

Dept- CHE

Name	Rajesh Ghosh	
Unique ID	1-1533646493	
Department	Department of Chemical Engineering.	
Date of joining	04.06.2012	
Education Qualification	M. TECH in Chemical Engineering from VTU, India.	
Work Experience	Working as an Assistant Professor in Department of Chemical Engineering.	
Area of Research	<ul style="list-style-type: none">• HydrothermalLiquefraction.• Metabolic pathwayofAlgae.	
Publications	Published more than 12 papers in various peer reviewed International and National Journals and several International and National conference proceedings.	
Subjects taught	<ul style="list-style-type: none">• Mechanicaloperation.• Processheattransfer.• Chemical Engineering Thermodynamics.• ReactionEngineering.	

Dept- CHE

Name	SwapnenduChatterjee	
Unique ID	1-5032545607	
Department	Department of Chemical Engineering	
Date of joining	22.08.2017	
Education Qualification	M. E in Chemical Engineering from Jadavpur University, India.	
Work Experience	Working as an Assistant Professor in Department of Chemical Engineering	
Area of Research	<ul style="list-style-type: none">• WasteBiomassValorization.• Hydrolysis.	
Publications	Published 4 papers in various peer reviewed International Journals and several International and National conference proceedings.	
Subjects taught	<ul style="list-style-type: none">• ChemicalReactionEngineering.• Processheattransfer.• FluidMechanics.• MassTransfer.	

Dept-CHE

Name: ZINNIA CHOWDHURY

Date of Birth: 23/04/1996

Mob Number: 6289463580

Email: chowdhuryzin@gmail.com

Unique ID- 1-11068975221

Doctorate Degree: NA

Master Degree: M.E

Bachelors Degree: B.Tech

Other Qualification's:NA

Field of Specialization: Membrane science, Pyrolysis

Date of joining the Institute: 21/12/2021

Appointment Type: (REGULAR/ADHOC/CONTRACT)-Regular

Exact Designation: Assistant Professor

Department:Chemical Engineering

Course:Chemical Engineering

Teaching for UG / PG/ Diploma: UG & PG

FY/Common Subject Teacher?

FY/Common Subject Name: N/A

Dept-CHE

Name: ANINDITA LAHA

Date of Birth: 19/02/1988

Mob Number:8688670961

Email:anindita.laha19@gmail.com

Unique ID- 1-11068975228

Doctorate Degree: Ph.D

Master Degree: M.Tech

Bachelors Degree: B.Tech

Other Qualification's: NA

Field of Specialization: Drug delivery, Nanotechnology

Date of joining the Institute: 18/1/2022

Appointment Type: (REGULAR/ADHOC/CONTRACT)-Regular

Exact Designation: Assistant Professor

Department:Chemical Engineering

Course:Chemical Engineering

Teaching for UG / PG/ Diploma: UG & PG

FY/Common Subject Teacher?

FY/Common Subject Name:N/A

NBA & NAAC Accreditation Status

NBA Accreditation Status		
1	Name/ List of Programmes/ Courses Accredited	N.A
2	Applied for Accreditation	WILL BE APPLIED
A.	Applied but Visit not happened	N.A
B.	Visit happened but result awaited	N.A

NAAC Accreditation Status		
1	Accredited	N.A
2	Applied for Accreditation	N.A
A.	Applied but Visit not happened	N.A
B.	Visit happened but result awaited	N.A